

MATHEMATICS – 4

(For Electrical, Electronics and Applied Electronics)

CODE	COURSE NAME	CATEGORY	L	T	P	CREDIT
MAT 204	PROBABILITY, RANDOM PROCESSES AND NUMERICAL METHODS	BASIC SCIENCE COURSE	3	1	0	4

Preamble: This course introduces students to the modern theory of probability and statistics, covering important models of random variables and analysis of random processes using appropriate time and frequency domain tools. A brief course in numerical methods familiarises students with some basic numerical techniques for finding roots of equations, evaluating definite integrals solving systems of linear equations and solving ordinary differential equations which are especially useful when analytical solutions are hard to find.

Prerequisite: A basic course in one-variable and multi-variable calculus.

Course Outcomes: After the completion of the course the student will be able to

CO 1	Understand the concept, properties and important models of discrete random variables and, using them, analyse suitable random phenomena.
CO 2	Understand the concept, properties and important models of continuous random variables and, using them, analyse suitable random phenomena.
CO 3	Analyse random processes using autocorrelation, power spectrum and Poisson process model as appropriate.
CO 4	Compute roots of equations, evaluate definite integrals and perform interpolation on given numerical data using standard numerical techniques
CO 5	Apply standard numerical techniques for solving systems of equations, fitting curves on given numerical data and solving ordinary differential equations.

Mapping of course outcomes with program outcomes

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
CO 1	3	2	2	2	2					2		1
CO 2	3	2	2	2	2					2		1
CO 3	3	2	2	2	2					2		1
CO 4	3	2	2	2	2					2		1
CO 5	3	2	2	2	2					2		1

Assessment Pattern

Bloom's Category	Continuous Assessment Tests(%)		End Semester Examination(%)
	1	2	
Remember	10	10	10
Understand	30	30	30
Apply	30	30	30
Analyse	20	20	20
Evaluate	10	10	10
Create			

End Semester Examination Pattern: There will be two parts; Part A and Part B. Part A contain 10 questions with 2 questions from each module, having 3 marks for each question. Students should answer all questions. Part B contains 2 questions from each module of which student should answer any one. Each question can have maximum 2 sub-divisions and carry 14 marks.

Course Level Assessment Questions

Course Outcome 1 (CO1):

1. Let X denote the number that shows up when an unfair die is tossed. Faces 1 to 5 of the die are equally likely, while face 6 is twice as likely as any other. Find the probability distribution, mean and variance of X .
2. An equipment consists of 5 components each of which may fail independently with probability 0.15. If the equipment is able to function properly when at least 3 of the components are operational, what is the probability that it functions properly?
3. X is a binomial random variable $B(n, p)$ with $n = 100$ and $p = 0.1$. How would you approximate it by a Poisson random variable?
4. Three balls are drawn at random without replacement from a box containing 2 white, 3 red and 4 black balls. If X denotes the number of white balls drawn and Y denotes the number of red balls drawn, find the joint probability distribution of (X, Y)

Course Outcome 2 (CO2)

1. What can you say about $P(X = a)$ for any real number a when X is (i) a discrete random variable? (ii) a continuous random variable?
2. A string, 1 meter long, is cut into two pieces at a random point between its ends. What is the probability that the length of one piece is at least twice the length of the other?

3. A random variable has a normal distribution with standard deviation 10. If the probability that it will take on a value less than 82.5 is 0.82, what is the probability that it will take on a value more than 58.3?
4. X and Y are independent random variables with X following an exponential distribution with parameter μ and Y following an exponential distribution with parameter λ . Find $P(X + Y \leq 1)$

Course Outcome 3(CO3):

1. A random process $X(t)$ is defined by $a \cos(\omega t + \theta)$ where a and ω are constants and θ is uniformly distributed in $[0, 2\pi]$. Show that $X(t)$ is WSS
2. How are the autocorrelation function and power spectral density of a WSS process related to each other?
3. Find the power spectral density of the WSS random process $X(t)$, given the autocorrelation function $R_X(\tau) = 9e^{-|\tau|}$
4. A conversation in a wireless ad-hoc network is severely disturbed by interference signals according to a Poisson process of rate $\lambda = 0.01$ per minute. (a) What is the probability that no interference signals occur within the first two minutes of the conversation? (b) Given that the first two minutes are free of disturbing effects, what is the probability that in the next minute precisely 1 interfering signal disturbs the conversation? (c) Given that there was only 1 interfering signal in the first 3 minutes, what is the probability that there would be at most 2 disturbances in the first 4 minutes?

Course Outcome 4(CO4):

1. Use Newton-Raphson method to find a real root of the equation $f(x) = e^{2x} - x - 6$ correct to 4 decimal places.
2. Compare Newton's divided difference method and Lagrange's method of interpolation.
3. Use Newton's forward interpolation formula to compute the approximate values of the function f at $x = 0.25$ from the following table of values of x and $f(x)$

x	0	0.5	1	1.5	2
f(x)	1.0000	1.0513	1.1052	1.1618	1.2214

4. Find a polynomial of degree 3 or less the graph of which passes through the points $(-1, 3)$, $(0, -4)$, $(1, 5)$ and $(2, -6)$

Course Outcome 5 (CO5):

1. Apply Gauss-Seidel method to solve the following system of equations

$$\begin{aligned} 4x_1 - x_2 - x_3 &= 3 \\ -2x_1 + 6x_2 + x_3 &= 9 \\ -x_1 + x_2 + 7x_3 &= -6 \end{aligned}$$

2. Using the method of least squares fit a straight line of the form $y = ax + b$ to the following set of ordered pairs (x, y) :
(2,4), (3,5), (5,7), (7,10), (9,15)
3. Write the normal equations for fitting a curve of the form $y = a_0 + a_1x^2$ to a given set of pairs of data points.
4. Use Runge-Kutta method of fourth order to compute $y(0.25)$ and $y(0.5)$, given the initial value problem

$$y' = x + xy + y, y(0) = 1$$

Syllabus

Module 1 (Discrete probability distributions) 9 hours

(Text-1: Relevant topics from sections-3.1-3.4, 3.6, 5.1)

Discrete random variables and their probability distributions, Expectation, mean and variance, Binomial distribution, Poisson distribution, Poisson approximation to the binomial distribution, Discrete bivariate distributions, marginal distributions, Independent random variables, Expectation (multiple random variables)

Module 2 (Continuous probability distributions) 9 hours

(Text-1: Relevant topics from sections-4.1-4.4, 3.6, 5.1)

Continuous random variables and their probability distributions, Expectation, mean and variance, Uniform, exponential and normal distributions, Continuous bivariate distributions, marginal distributions, Independent random variables, Expectation (multiple random variables), i. i. d random variables and Central limit theorem (without proof).

Module 3 (Random Processes) 9 hours

(Text-2: Relevant topics from sections-8.1-8.5, 8.7, 10.5)

Random processes and classification, mean and autocorrelation, wide sense stationary (WSS) processes, autocorrelation and power spectral density of WSS processes and their properties, Poisson process-distribution of inter-arrival times, combination of independent Poisson processes (merging) and subdivision (splitting) of Poisson processes (**results without proof**).

Module 4 (Numerical methods -I) 9 hours**(Text 3- Relevant topics from sections 19.1, 19.2, 19.3, 19.5)**

Errors in numerical computation-round-off, truncation and relative error, Solution of equations – Newton-Raphson method and Regula-Falsi method. Interpolation-finite differences, Newton's forward and backward difference method, Newton's divided difference method and Lagrange's method. Numerical integration-Trapezoidal rule and Simpson's 1/3rd rule (**Proof or derivation of the formulae not required for any of the methods in this module**)

Module 5 (Numerical methods -II)**9 hours****(Text 3- Relevant topics from sections 20.3, 20.5, 21.1)**

Solution of linear systems-Gauss-Seidel and Jacobi iteration methods. Curve fitting-method of least squares, fitting straight lines and parabolas. Solution of ordinary differential equations-Euler and Classical Runge-Kutta method of second and fourth order, Adams-Moulton predictor-correction method (**Proof or derivation of the formulae not required for any of the methods in this module**)

Text Books

1. (Text-1) Jay L. Devore, *Probability and Statistics for Engineering and the Sciences*, 8th edition, Cengage, 2012
2. (Text-2) Oliver C. Ibe, *Fundamentals of Applied Probability and Random Processes*, Elsevier, 2005.
3. (Text-3) Erwin Kreyszig, *Advanced Engineering Mathematics*, 10 th Edition, John Wiley & Sons, 2016.

Reference Books

1. Hossein Pishro-Nik, *Introduction to Probability, Statistics and Random Processes*, Kappa Research, 2014 (Also available online at www.probabilitycourse.com)
2. V.Sundarapandian, *Probability, Statistics and Queueing theory*, PHI Learning, 2009
3. Gubner, *Probability and Random Processes for Electrical and Computer Engineers*, Cambridge University Press,2006.
4. B.S. Grewal, *Higher Engineering Mathematics*, Khanna Publishers, 36 Edition, 2010.

Assignments

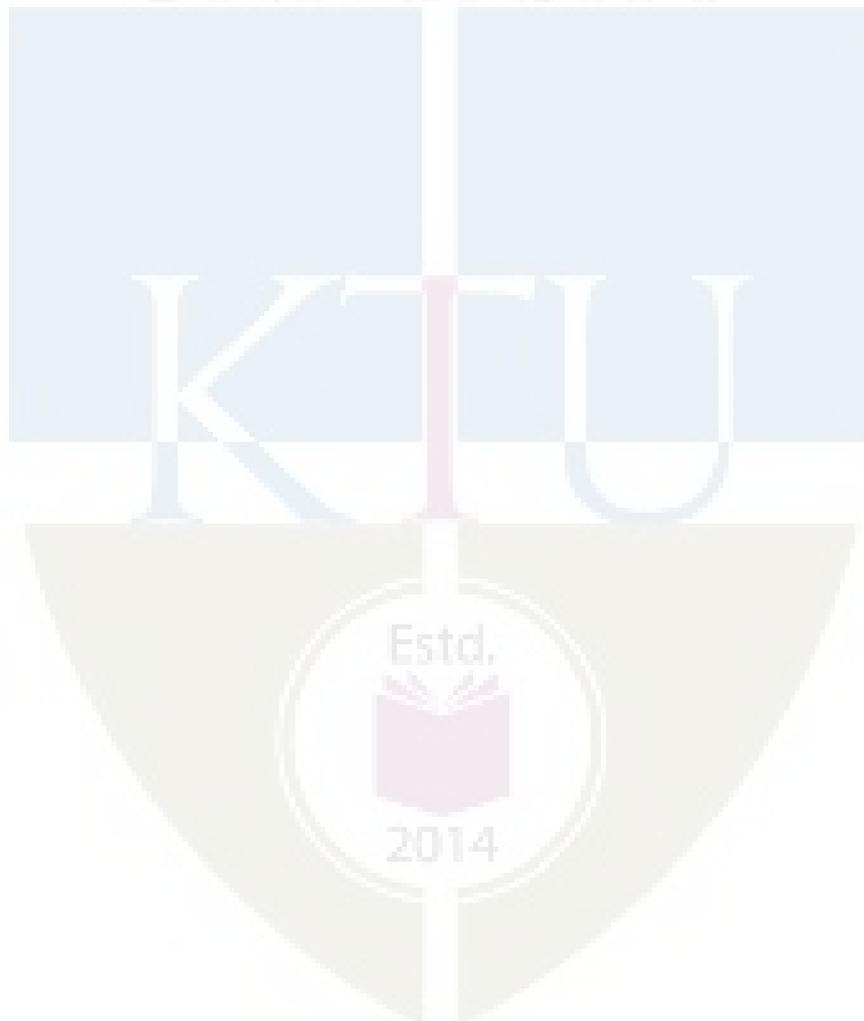
Assignments should include specific problems highlighting the applications of the methods introduced in this course in physical sciences and engineering.

Course Contents and Lecture Schedule

No	Topic	No. of Lectures
1	Discrete Probability distributions	9 hours
1.1	Discrete random variables and probability distributions, expected value, mean and variance (discrete)	3
1.2	Binomial distribution-mean, variance, Poisson distribution-mean, variance, Poisson approximation to binomial	3
1.3	Discrete bivariate distributions, marginal distributions, Independence of random variables (discrete), Expected values	3
2	Continuous Probability distributions	9 hours
2.1	Continuous random variables and probability distributions, expected value, mean and variance (continuous)	2
2.2	Uniform, exponential and normal distributions, mean and variance of these distributions	4
2.3	Continuous bivariate distributions, marginal distributions, Independent random variables, Expected values, Central limit theorem.	3
3	Random processes	9 hours
3.1	Random process -definition and classification, mean , autocorrelation	2
3.2	WSS processes its autocorrelation function and properties	2
3.3	Power spectral density	2
3.4	Poisson process, inter-distribution of arrival time, merging and splitting	3
4	Numerical methods-I	9 hours
4.1	Roots of equations- Newton-Raphson, regulafalsi methods	2
4.2	Interpolation-finite differences, Newton's forward and backward formula,	3
4.3	Newton's divided difference method, Lagrange's method	2
4.3	Numerical integration-trapezoidal rule and Simpson's 1/3-rd rule	2
5	Numerical methods-II	9 hours
5.1	Solution of linear systems-Gauss-Siedal method, Jacobi iteration	2

	method	
5.2	Curve-fitting-fitting straight lines and parabolas to pairs of data points using method of least squares	2
5.3	Solution of ODE-Euler and Classical Runge-Kutta methods of second and fourth order	4
5.4	Adams-Moulton predictor-corrector method	1

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Model Question Paper
(2019 Scheme)

Reg No:
Name:

Total Pages: 3

APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY

FOURTH SEMESTER B.TECH DEGREE EXAMINATION
(Month & year)

Course Code: MAT 204

Course Name: PROBABILITY, RANDOM PROCESSES AND NUMERICAL METHODS

(For (i) Electrical and Electronics, (ii) Electronics and Communication, (iii) Applied Electronics and Instrumentation Engineering branches)

Max Marks :100

Duration : 3 Hours

PART A

(Answer all questions. Each question carries 3 marks)

1. Suppose X is binomial random variable with parameters $n = 100$ and $p = 0.02$. Find $P(X < 3)$ using Poisson approximation to X . (3)
2. The diameter of circular metallic discs produced by a machine is a random variable with mean 6cm and variance 2cm. Find the mean area of the discs. (3)
3. Find the mean and variance of the continuous random variable X with probability density function (3)

$$f(x) = \begin{cases} 2x - 4, & 2 \leq x \leq 3 \\ 0 & \text{otherwise} \end{cases}$$
4. The random variable X is exponentially distributed with mean 3. Find $P(X > t + 3 | X > t)$ where t is any positive real number. (3)
5. Give any two examples of a continuous time discrete state random processes. (3)
6. How will you calculate the mean, variance and total power of a WSS process from its autocorrelation function? (3)
7. Find all the first and second order forward and backward differences of y for the following set of (x, y) values: (0.5, 1.13), (0.6, 1.19), (0.7, 1.26), (0.8, 1.34) (3)
8. The following table gives the values of a function $f(x)$ for certain values of x . (3)

x	0	0.25	0.50	0.75	1
$f(x)$	1	0.9412	0.8	0.64	0.5

Evaluate $\int_0^1 f(x)dx$ using trapezoidal rule.

9. Explain the principle of least squares for determining a line of best fit to a given data (3)
10. Given the initial value problem $y' = y + x$, $y(0) = 0$, find $y(0.1)$ and $y(0.2)$ using Euler method. (3)

PART B

(Answer one question from each module)

MODULE 1

11. (a) The probability mass function of a discrete random variable is $p(x) = kx, x = 1, 2, 3$ where k is a positive constant. Find (i) the value of k (ii) $P(X \leq 2)$ (iii) $E[X]$ and (iv) $\text{var}(1 - X)$. (7)
- (b) Find the mean and variance of a binomial random variable (7)

OR

12. (a) Accidents occur at an intersection at a Poisson rate of 2 per day. What is the probability that there would be no accidents on a given day? What is the probability that in January there are at least 3 days (not necessarily consecutive) without any accidents? (7)
- (b) Two fair dice are rolled. Let X denote the number on the first die and $Y = 0$ or 1 , according as the first die shows an even number or odd number. Find (i) the joint probability distribution of X and Y , (ii) the marginal distributions. (iii) Are X and Y independent? (7)

MODULE 2

13. (a) The IQ of an individual randomly selected from a population is a normal distribution with mean 100 and standard deviation 15. Find the probability that an individual has IQ (i) above 140 (ii) between 120 and 130. (7)
- (b) A continuous random variable X is uniformly distributed with mean 1 and variance $4/3$. Find $P(X < 0)$ (7)

OR

14. (a) The joint density function of random variables X and Y is given by (7)

$$f(x, y) = \begin{cases} e^{-(x+y)}, & x > 0, \quad y > 0 \\ 0 & \text{otherwise.} \end{cases}$$

Find $P(X + Y \leq 1)$. Are X and Y independent? Justify.

- (b) The lifetime of a certain type of electric bulb may be considered as an exponential random variable with mean 50 hours. Using central limit theorem, find the approximate probability that 100 of these electric bulbs will provide a total of more than 6000 hours of burning time. (7)

MODULE 3

15. (a) A random process $X(t)$ is defined by $X(t) = Y(t) \cos(\omega t + \Theta)$ where $Y(t)$ is a WSS process, ω is a constant and Θ is uniformly distributed in $[0, 2\pi]$ and is independent of $Y(t)$. Show that $X(t)$ is WSS (7)
- (b) Find the power spectral density of the random process $X(t) = a \sin(\omega_0 t + \Theta)$, ω_0 constant and Θ is uniformly distributed in $(0, 2\pi)$ (7)

OR

16. Cell-phone calls processed by a certain wireless base station arrive according to a Poisson process with an average of 12 per minute. (7)
- (a) What is the probability that more than three calls arrive in an interval of length 20 seconds? (7)
- (b) What is the probability that more than 3 calls arrive in each of two consecutive intervals of length 20 seconds? (7)

MODULE 4

17. (a) Use Newton-Raphson method to find a non-zero solution of $x = 2 \sin x$. Start with $x_0 = 1$ (7)
 (b) Using Lagrange's interpolating polynomial estimate $f(1.5)$ for the following data (7)

x	0	1	2	3
$y = f(x)$	0	0.9826	0.6299	0.5532

OR

18. (a) Consider the data given in the following table (7)

x	0	0.5	1	1.5	2
$f(x)$	1.0000	1.0513	1.1052	1.1618	1.2214

Estimate the value of $f(1.80)$ using Newton's backward interpolation formula.

- (b) Evaluate $\int_0^1 e^{-x^2/2} dx$ using Simpson's one-third rule, dividing the interval $[0, 1]$ into 8 subintervals (7)

MODULE 5

19. (a) Using Gauss-Seidel method, solve the following system of equations (7)

$$\begin{aligned} 20x + y - 2z &= 17 \\ 3x + 20y - z &= -18 \\ 2x - 3y + 20z &= 25 \end{aligned}$$

- (b) The table below gives the estimated population of a country (in millions) for during 1980-1995 (7)

year	1980	1985	1990	1995
population	227	237	249	262

Plot a graph of this data and fit an appropriate curve to the data using the method of least squares. Hence predict the population for the year 2010.

OR

20. (a) Use Runge-Kutta method of fourth order to find $y(0.2)$ given the initial value problem (7)

$$\frac{dy}{dx} = \frac{xy}{1+x^2}, \quad y(0) = 1$$

Take step-size, $h = 0.1$.

- (b) Solve the initial value problem (7)

$$\frac{dy}{dx} = x + y, \quad y(0) = 0,$$

in the interval $0 \leq x \leq 1$, taking step-size $h = 0.2$. Calculate $y(0.2), y(0.4)$ and $y(0.6)$ using Runge-Kutta second order method, and $y(0.8)$ and $y(1.0)$ using Adam-Moulton predictor-corrector method.

ECT202	ANALOG CIRCUITS	CATEGORY	L	T	P	CREDIT
		PCC	3	1	0	4

Preamble: This course aims to develop the skill of analyse and design of different types of analog circuits using discrete electronic components.

Prerequisite: EST130 Basics of Electrical and Electronics Engineering

Course Outcomes: After the completion of the course the student will be able to

CO 1	Design analog signal processing circuits using diodes and first order RC circuit
CO 2	Analyse basic amplifiers using BJT and MOSFET
CO 3	Apply the principle of oscillator and regulated power supply circuits.

Mapping of course outcomes with program outcomes

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
CO 1	3	3										2
CO 2	3	3										2
CO 3	3	3										2

Assessment Pattern

Bloom's Category		Continuous Assessment Tests		End Semester Examination
		1	2	
Remember	K1	10	10	10
Understand	K2	20	20	20
Apply	K3	20	20	70
Analyse	K4			
Evaluate				
Create				

Mark distribution

Total Marks	CIE	ESE	ESE Duration
150	50	100	3 hours

Continuous Internal Evaluation Pattern:

Attendance	: 10 marks
Continuous Assessment Test (2 numbers)	: 25 marks
Assignment/Quiz/Course project	: 15 marks

End Semester Examination Pattern: There will be two parts; Part A and Part B. Part A contain 10 questions with 2 questions from each module, having 3 marks for each question. Students should answer all questions. Part B contains 2 questions from each module of which student should answer any one. Each question can have maximum 2 sub-divisions and carry 14 marks.

Course Level Assessment Questions

Course Outcome 1 (CO1): Design analog signal processing circuits using diodes and first order RC circuit.

1. For the given specification design a differentiator / integrator circuit.
2. For the given transfer characteristics design clipping / clamping circuit.
3. Design first order RC low-pass / high-pass circuit for the given specification.

Course Outcome 2 (CO2): Analyse basic amplifiers using BJT.

1. For the given transistor biasing circuit, determine the resistor values, biasing currents and voltages.
2. Design a RC coupled amplifier for a given gain.
3. Analyse the frequency response of BJT RC coupled amplifier using hybrid π model.

Course Outcome 2 (CO2): Analyse basic amplifiers using MOSFET.

1. Perform DC analysis of MOSFET circuits.
2. Design a common source amplifier.
3. Deduce the expression for voltage gain of CS stage with diode-connected load.

Course Outcome 2 (CO2): Analyse basic feedback amplifiers using BJT and MOSFET

1. Deduce the expression for voltage gain, input impedance and output impedance of the four feedback amplifier topologies.
2. Design practical discrete amplifiers for the four feedback amplifier topologies.

Course Outcome 3 (CO3): Apply the principle of oscillator and regulated power supply.

1. Design oscillator using BJT to generate sine wave for the given frequency.
2. Deduce the expression for maximum efficiency of class B power amplifiers.
3. Illustrate the DC and AC load line in transformer coupled class A power amplifiers.
4. Design voltage regulator for the given specifications.

ELECTRONICS AND COMMUNICATION ENGINEERING
SYLLABUS

Module 1:

Wave shaping circuits: First order RC differentiating and integrating circuits, First order RC low pass and high pass filters.

Diode Clipping circuits - Positive, negative and biased clipper. Diode Clamping circuits - Positive, negative and biased clamper.

Transistor biasing: Need, operating point, concept of DC load line, fixed bias, self bias, voltage divider bias, bias stabilization.

Module 2:

BJT Amplifiers: RC coupled amplifier (CE configuration) – need of various components and design, Concept of AC load lines, voltage gain and frequency response.

Small signal analysis of CE configuration using small signal hybrid-pi model for mid frequency and low frequency. (gain, input and output impedance).

High frequency equivalent circuits of BJT, Miller effect, Analysis of high frequency response of CE amplifier.

Module 3:

MOSFET amplifiers: MOSFET circuits at DC, MOSFET as an amplifier, Biasing of discrete MOSFET amplifier, small signal equivalent circuit. Small signal voltage and current gain, input and output impedance of CS configuration. CS stage with current source load, CS stage with diode-connected load.

Multistage amplifiers - effect of cascading on gain and bandwidth. Cascode amplifier.

Module 4 :

Feedback amplifiers: Effect of positive and negative feedback on gain, frequency response and distortion. The four basic feedback topologies, Analysis of discrete BJT circuits in voltage-series and voltage-shunt feedback topologies - voltage gain, input and output impedance.

Oscillators: Classification, criterion for oscillation, Wien bridge oscillator, Hartley and Crystal oscillator. (working principle and design equations of the circuits; analysis of Wien bridge oscillator only required).

Module 5:

Power amplifiers: Classification, Transformer coupled class A power amplifier, push pull class B and class AB power amplifiers, complementary-symmetry class B and Class AB power amplifiers, efficiency and distortion (no analysis required)

Regulated power supplies: Shunt voltage regulator, series voltage regulator, Short circuit protection and fold back protection, Output current boosting.

Text Books

1. Robert Boylestad and L Nashelsky, "Electronic Devices and Circuit Theory", 11/e Pearson, 2015.
2. Sedra A. S. and K. C. Smith, "Microelectronic Circuits", 6/e, Oxford University Press, 2013.

Reference Books

1. Razavi B., "Fundamentals of Microelectronics", Wiley, 2015
2. Neamen D., "Electronic Circuits, Analysis and Design", 3/e, TMH, 2007.
3. David A Bell, "Electronic Devices and Circuits", Oxford University Press, 2008.
4. Rashid M. H., "Microelectronic Circuits - Analysis and Design", Cengage Learning, 2/e, 2011
5. Millman J. and C. Halkias, "Integrated Electronics", 2/e, McGraw-Hill, 2010.

Course Contents and Lecture Schedule

No	Topic	No. of lectures
1	Wave shaping circuits	
1.1	Analysis and design of RC differentiating and integrating circuits	2
1.2	Analysis and design of First order RC low pass and high pass filters	2
1.3	Clipping circuits - Positive, negative and biased clipper	1
1.4	Clamping circuits - Positive, negative and biased clamper	1
	Transistor biasing	
1.5	Need of biasing, operating point, bias stabilization, concept of load line	1
	Design of fixed bias, self bias, voltage divider bias.	2
2	BJT Amplifiers	
2.1	Classification of amplifiers, RC coupled amplifier (CE configuration) – need of various components and design, Concept of AC load lines.	2
2.2	Small signal analysis of CE configuration using small signal hybrid π model for mid frequency. (gain, input and output impedance).	3
2.3	High frequency equivalent circuits of BJT, Miller effect, Analysis of high frequency response of CE amplifier. voltage gain and frequency response	4
3	MOSFET amplifiers	
3.1	MOSFET circuits at DC, MOSFET as an amplifier, Biasing of discrete MOSFET amplifier,	2
3.2	Small signal equivalent circuit. Small signal voltage and current gain, input and output impedances of CS configuration.	3

3.3	CS stage with current source load, CS stage with diode-connected load.	2
3.4	Multistage amplifiers - effect of cascading on gain and bandwidth. Cascode amplifier.	2
4	Feedback amplifiers	
4.1	Properties of positive and negative feedback on gain, frequency response and distortion.	1
4.2	Analysis of the four basic feedback topologies	2
4.3	Analysis of discrete circuits in each feedback topologies -voltage gain, input and output impedance	3
	Oscillators	
4.4	Classification, criterion for oscillation	1
	Wien bridge oscillator, Hartley and Crystal oscillator. (working principle and design equations of the circuits; analysis not required).	2
5	Power amplifiers	
5.1	Classification, Transformer coupled class A power amplifier	1
5.2	push pull class B and class AB power amplifiers, complementary-symmetry class B and Class AB power amplifiers, efficiency and distortion (no analysis required)	3
	Linear Regulated power supplies	
5.3	Principle of Linear Regulated power supplies, Shunt voltage regulator	1
5.4	Series voltage regulator, Short circuit protection and fold back protection, Output current boosting	2

Assignment:

Atleast one assignment should be simulation of different types of transistor amplifiers on any circuit simulation software.

Estd.

2014

Model Question paper**APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY**THIRD SEMESTER B.TECH DEGREE EXAMINATION, (**Model Question Paper**)**Course Code: ECT202****Course Name: ANALOG CIRCUITS**

Max. Marks: 100

Duration: 3 Hours

PART A

Answer ALL Questions. Each Carries 3 mark.

- | | | | |
|---|---|---|----|
| 1 | Design the first order RC high pass filter with cut off frequency 2Kz. | 3 | K3 |
| 2 | Describe about the double ended clipping. | 3 | K2 |
| 3 | Differentiate between DC and AC load lines. | 3 | K2 |
| 4 | What is the significance of Miller effect on high frequency amplifiers? | 3 | K1 |
| 5 | What are the effects of cascading in gain and bandwidth of an amplifier? | 3 | K1 |
| 6 | Calculate the drain current if $\mu_n C_{ox} = 100 \mu A/V^2$, $V_{TH} = 0.5V$ and $\lambda = 0$ in the following circuit. | 3 | K3 |



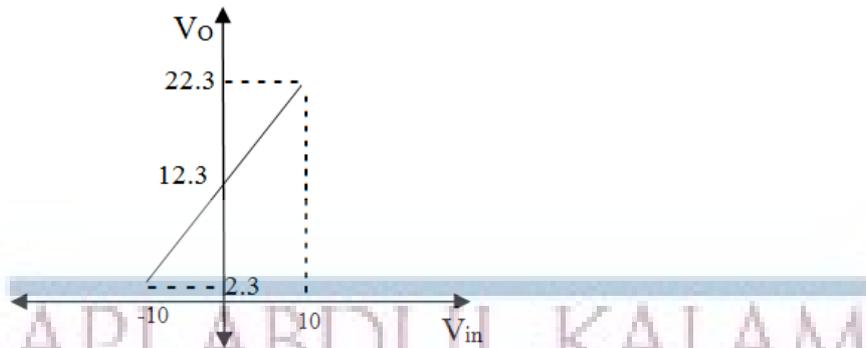
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|----|--|---|----|
| 7 | Illustrate the effect of negative feedback on bandwidth and gain of the amplifier. | 3 | K2 |
| 8 | Explain the criteria for an oscillator to oscillate. | 3 | K1 |
| 9 | How to eliminate cross over distortion in class-B power amplifier? | 3 | K2 |
| 10 | What is line regulation and load regulation in the context of a voltage regulator? | 3 | K2 |

PART – B

Answer one question from each module; each question carries 14 marks.

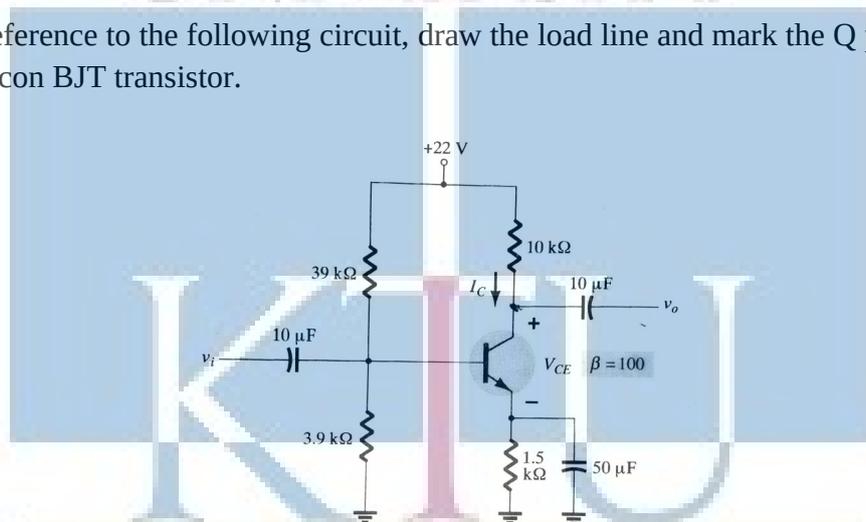
Module - I

- | | | | |
|------|---|---|-----------|
| 11 a | Design a differentiator circuit for a square wave signal with $V_{pp}=10$ and frequency 10KHz. | 6 | CO1
K3 |
| b. | Design a clamper circuit to get the following transfer characteristics, assuming voltage drop across the diodes 0.7V. | 8 | CO1
K3 |



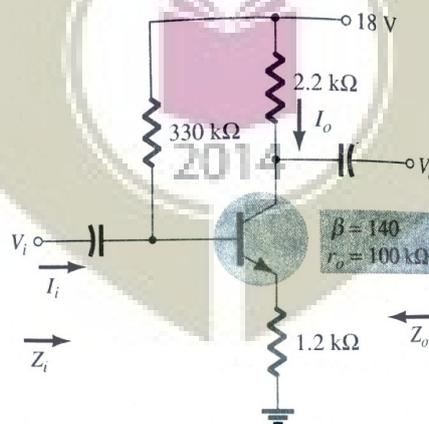
OR

- 12 a Explain the working of an RC differentiator circuit for a square wave input with period T . Sketch its output waveform for $RC \gg T$, $RC \ll T$ and $RC = T$. 5 K2
CO1
- b. With reference to the following circuit, draw the load line and mark the Q point of the Silicon BJT transistor. 9 K3
CO2



Module - II

- 13 For the following RC coupled amplifier determine r_e , Z_i , Z_o and A_v . 14 K3
CO2



OR

ELECTRONICS AND COMMUNICATION ENGINEERING

- 14 a Draw the high frequency hybrid π model of BJT in CE configuration and explain the significance of each parameter. 6 K2
CO2
- b Analyse BJT RC coupled amplifier in CE configuration at high frequency using hybrid π model. 8 K2
CO2

Module - III

- 15 a Draw the circuit of a common source amplifier using MOSFET. Derive the expressions for voltage gain and input resistance from small signal equivalent circuit. 7 K2
CO2
- b. How wide bandwidth is obtained in Cascode amplifier ? 7 K2
CO2

OR

- 16 Draw the CS stage with current source load and deduce the expression for voltage gain of the amplifier 14 K3
CO2

Module - IV

- 17 Give the block schematic of current-series feedback amplifier configuration and deduce the expression for gain, input impedance and output impedance with feedback. Design a practical circuit for this current-series feedback amplifier. 14 K3
CO2

OR

- 18 a Design wein-bridge oscillator using BJT to generate 1KHz sine wave. 8 K3
CO3
- b Explain the working principle of crystal oscillator 6 K2
CO3

Module - V

- 19 Illustrate the working principle of complementary-symmetry class B power amplifiers and deduce the maximum efficiency of the circuit 14 K2
CO2

OR

- 20 Design a discrete series voltage regulator with short circuit protection for regulated output voltage 10V and maximum current 100mA. 14 K3
CO3

Simulation Assignments (ECT202)

The following simulations can be done in QUCS, KiCad or PSPICE.

1. Design and simulate a voltage series feedback amplifier based on BJT/ MOSFET. Observe the input and output signals. Plot the AC frequency response. Observe the Nyquits plot and understand its stability
2. Design and simulate a voltage shunt feedback amplifier based on BJT/ MOSFET. Observe the input and output signals. Plot the AC frequency response. Observe the Nyquits plot and understand its stability
3. Design and simulate series voltage regulator for output voltage $V_O = 10V$ and output current $I_O = 100mA$ with and without short circuit protection and to test the line and load regulations.
4. Design and simulate Wien bridge oscillator for a frequency of $5 kHz$. Run a transient simulation and observe the output waveform.
5. Design and simulate Colpitts oscillator for a frequency of $455 kHz$. Run a transient simulation and observe the output waveform.
6. Design and simulate a current series feedback amplifier based on BJT. Observe the input and output signals. Plot the AC frequency response. Observe the Nyquits plot and understand its stability
7. Design and simulate Hartley oscillator for a frequency of $455 kHz$. Run a transient simulation and observe the output waveform.
8. Design and simulate clipping circuits that clips the $10 V$ input sinusoid
 - at $+3.5 V$ and at $-4.2 V$
 - at $+2.5 V$ and at $+4.2 V$
 - at $-2.5 V$ and at $-4.2 V$

with Si diodes

ECT 204	SIGNALS AND SYSTEMS	CATEGORY	L	T	P	CREDIT
		PCC	3	1	0	4

Preamble: This course aims to lay the foundational aspects of signals and systems in both continuous time and discrete time, in preparation for more advanced subjects in digital signal processing, image processing, communication theory and control systems.

Prerequisite: Nil

Course Outcomes: After the completion of the course the student will be able to

CO 1	Apply properties of signals and systems to classify them
CO 2	Represent signals with the help of series and transforms
CO 3	Describe orthogonality of signals and convolution integral.
CO 4	Apply transfer function to compute the LTI response to input signals.
CO 5	Apply sampling theorem to discretize continuous time signals

Mapping of course outcomes with program outcomes

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
CO 1	3	3										
CO 2	3	3	3									
CO 3	3	3	3									
CO 4	3	3										
CO 5	3	3	3									

Assessment Pattern

Bloom's Category	Continuous Assessment Tests		End Semester Examination
	1	2	
Remember	10	10	10
Understand	20	20	20
Apply	20	20	70
Analyse			
Evaluate			
Create			

Mark distribution

Total	CIE	ESE	ESE Duration
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Marks			
150	50	100	3 hours

Continuous Internal Evaluation Pattern:

Attendance : 10 marks

Continuous Assessment Test (2 numbers) : 25 marks

Assignment/Quiz/Course project : 15 marks

End Semester Examination Pattern: There will be two parts; Part A and Part B. Part A contain 10 questions with 2 questions from each module, having 3 marks for each question. Students should answer all questions. Part B contains 2 questions from each module of which student should answer any one. Each question can have maximum 2 sub-divisions and carry 14 marks.

Course Level Assessment Questions**Course Outcome 1 (CO1) : Apply properties of signals and systems to classify them**

1. Check whether the following systems are stable, causal, linear, and time-invariant (a) $y[n] = x[2n]$ (b) $y(t) = x^2(t) + 3$ (c) $y[n] = nx[n]$
2. Plot (a) $u(t-1) + u(1-t)$ (b) $u(t-1) - u(t+1)$ (c) $\text{sinc}(t/T)$ (d) $r(t) - r(t-2) - 2u(t-2)$

Course Outcome 2 (CO2) : Represent signals with the help of series and transforms

1. Compute the Fourier transform of (a) $x(t) = 1, -T/2 < t < T/2$, and 0 elsewhere (b) $x(t) = 1 - (|t|/T), -T < t < T$, and 0 elsewhere
2. Show that a square wave has only odd harmonics.
3. State and prove Parsevals theorem

Course Outcome 3 (CO3) : Describe orthogonality of signals and convolution integral.

1. Show that $\delta(t-a)$ and $\delta(t-b)$, $a \neq b$ are orthogonal
2. Define convolution of $x(t)$ and $h(t)$

Course Outcome 4 (CO4) : Apply transfer function to compute the LTI response to input signals.

1. Give the frequency response of a first-order low pass filter. What is the 3-dB cut off frequency?
2. What is the significance of linear phase response?

Course Outcome 5 (CO5) : Apply sampling theorem to discretize continuous time signals

1. Derive the interpolation formula for finite-energy band-limited signals from its samples.

SYLLABUS

Elementary signals, Continuous time and Discrete time signals and systems, Signal operations, Differential equation representation, Difference equation representation, Continuous time LTI Systems, Discrete time LTI Systems, Correlation between signals, Orthogonality of signals, Frequency domain representation, Continuous time Fourier series, Continuous time Fourier transform, Using Laplace transform to characterize Transfer function, Stability and Causality using ROC of Transfer transform, Frequency response, Sampling, Aliasing, Z transform, Inverse Z transform, Unilateral Z-transform, Frequency domain representation of discrete time signals, Discrete-time Fourier series and discrete time Fourier transform (DTFT), Analysis of discrete time LTI systems using the above transforms.

Text Books

1. Alan V. Oppenheim and Alan Willsky, Signals and Systems, PHI, 2/e, 2009
2. Simon Haykin, Signals & Systems, John Wiley, 2/e, 2003

Reference Books

1. Anand Kumar, Signals and Systems, PHI, 3/e, 2013.
2. B P. Lathi, Principles of Signal Processing & Linear systems, Oxford University Press.
3. Gurung, Signals and System, PHI.
4. Mahmood Nahvi, Signals and System, Mc Graw Hill (India), 2015.
5. P Ramakrishna Rao, Shankar Prakriya, Signals and System, MC Graw Hill Edn 2013.
6. Rodger E. Ziemer, Signals & Systems - Continuous and Discrete, Pearson, 4/e, 2013

Course Contents and Lecture Schedule 2014

Module	Topic	Number of lecture hours
I	Elementary Signals, Classification and representation of continuous time and discrete time signals, Signal operations	4
	Continuous time and discrete time systems – Classification, Properties.	3
	Representation of systems: Differential equation representation of continuous time systems. Difference equation representation of discrete systems.	2
	Continuous time LTI systems and convolution integral.	2

ELECTRONICS AND COMMUNICATION ENGINEERING

	Discrete time LTI systems and linear convolution.	2
	Stability and causality of LTI systems.	2
	Correlation between signals, Orthogonality of signals.	1
II	Frequency domain representation of continuous time signals - continuous time Fourier series and its properties.	4
	Continuous time Fourier transform and its properties. Convergence and Gibbs phenomenon	3
	Review of Laplace Transform, ROC of Transfer function, Properties of ROC, Stability and causality conditions.	3
	Relation between Fourier and Laplace transforms.	1
III	Analysis of LTI systems using Laplace and Fourier transforms. Concept of transfer function, Frequency response, Magnitude and phase response.	4
	Sampling of continuous time signals, Sampling theorem for lowpass signals, aliasing.	3
IV	Frequency domain representation of discrete time signals, Discrete time fourier series for discrete periodic signals. Properties of DTFS.	4
	Discrete time fourier transform (DTFT) and its properties. Analysis of discrete time LTI systems using DTFT. Magnitude and phase response.	5
V	Z transform, ROC , Inverse transform, properties, Unilateral Z transform.	3
	Relation between DTFT and Z-Transform, Analysis of discrete time LTI systems using Z transforms, Transfer function. Stability and causality using Z transform.	4



Simulation Assignments (ECT 204)

The following simulation assignments can be done with Python/MATLAB/ SCILAB/OCTAVE

1. Generate the following discrete signals
 - Impulse signal
 - Pulse signal and
 - Triangular signal
2. Write a function to compute the DTFT of a discrete energy signal. Test this function on a few signals and plot their magnitude and phase spectra.
3.
 - Compute the linear convolution between the sequences $x = [1, 3, 5, 3]$ with $h = [2, 3, 5, 6]$. Observe the stem plot of both signals and the convolution.
 - Now let $h = [1, 2, 1]$ and $x = [2, 3, 5, 6, 7]$. Compute the convolution between h and x .
 - Flip the signal x by 180° so that it becomes $[7, 6, 5, 3, 2]$. Convolve it with h . Compare the result with the previous result.
 - Repeat the above two steps with $h = [1, 2, 3, 2, 1]$ and $h = [1, 2, 3, 4, 5, 4, 3, 2, 1]$
 - Give your inference.
4.
 - Write a function to generate a unit pulse signal as a summation of shifted unit impulse signals
 - Write a function to generate a triangular signal as a convolution between two pulse signals.
5.
 - Relaise a continuous time LTI system with system response

$$H(s) = \frac{5(s+1)}{(s+2)(s+3)}$$

. One may use *scipy.signal.lti* package in Python.

- Make it into a discrete system (possibly with *scipy.signal.cont2discrete*)
- Observe the step response in both cases and compare.

Model Question Paper

A P J Abdul Kalam Technological University

Fourth Semester B Tech Degree Examination

Course: ECT 204 Signals and Systems

Time: 3 Hrs

Max. Marks: 100

PART A

Answer All Questions

- 1 Differentiate between energy and power signal with example. (3) K_2
- 2 Test if the signals $x_1[n] = [1, -2, 3, 1]$ and $x_2[n] = [-1, 2, 1, 2]$ are orthogonal. (3) K_3
- 3 Compute the Fourier transform of $x(t) = \delta(t) + 0.5\delta(t - 1)$ (3) K_2
- 4 Write the Fourier series for $x(t) = A \cos 2\pi f_c t$ and use it to plot its line spectrum (3) K_2
- 5 Explain the transfer function of an LTI system in the s - domain. (3) K_1
- 6 What is the discrete frequency resulting when a 2 kHz signal is sampled by an 8 kHz sampling signals? (3) K_2
- 7 Give three properties of the ROC pertaining to Z -transform. (3) K_1
- 8 Compute the DTFT of $x[n] = \delta[n] + 2\delta[n - 1] + 0.5\delta[n - 3]$ (3) K_3
- 9 Write the transfer function $H(z)$ of an LTI system described by (3) K_2

$$y[n] = 0.3y[n - 1] + 0.1y[n - 2] + x[n] + 0.2x[n - 1]$$
- 10 Give the relation between DTFT and Z transform (3) K_2

PART B

Answer one question from each module. Each question carries 14 mark.

Module I

- 11(A) Test if the following systems are stable and time invariant (8) K_3
 i. $y[n] = \cos x[n]$
 ii. $y[n] = x[n] - x[n - 1]$
- 11(B) Classify the following signals are energy and power signals (6) K_3
 i. $x[n] = 0.8^n U[n]$
 ii. $x[n] = U[n] - U[n - 10]$
 iii. $x[n] = \cos 2\pi f_0 n$

OR

- 12(A) Compute the convolution between $U[t] - U[t - 5]$ with itself. (7) K_3
 12(B) Compute the output of the LTI system with input $x[n] = [1, -1, 2, -2]$ and impulse response $h[n] = [1, 2, 1]$ (7) K_3

Module II

- 13(A) Compute the Fourier transform of the triangular signal (8) K_3
 $x(t) = A[1 - \frac{|t|}{T}]$
- 13(B) Compute the Fourier series of a half wave rectified sinusoid (6) K_3
 with period T and amplitude A

OR

- 14(A) Compute the Laplace transforms of (8) K_3
 i. $x(t) = 2e^{-t}U[t] + 0.5e^{-3t}U[t]$
 ii. $x(t) = 2e^{-3t} \cos 4tU[t]$
- 14(B) Compute the Fourier transform of a rectangular pulse with unit amplitude and width T and centred around origin. Plot the Fourier transform in the frequency domain. (6) K_3

Module III

- 15(A) Define sampling theorem. Determine the Nyquist rate and Nyquist interval for the signal (6) K_2

$$x(t) = \cos \pi t + 3 \sin 2\pi t + \sin 4\pi t$$

- 15(B) Analyze and characterize the LTI system $x(t)$ using Laplace Transform (8) K_2

$$x(t) = \frac{2}{3}e^{-t}u(t) + \frac{1}{3}e^{2t}u(t)$$

OR

- 16(A) Obtain the response of an LTI system with impulse response $h(t) = \delta(t)$ with input signal $x(t) = e^{-at}u(t)$ using Fourier transform (6) K_2

- 16(B) Explain spectral aliasing and the need for anti-aliasing filter with an example spectrum (8) K_2

Module IV

- 17(A) Describe the magnitude response and phase response of a discrete LTI system with the help of DTFTs. (7) K_2

- 17(B) Compute the magnitude response of an LTI system described by (7) K_2

$$y[n] = 0.1y[n-1] + 0.1y[n-3] + x[n] + 0.2x[n-1] + 0.1x[n-2]$$

in terms of the DTFTs

OR

- 18 An LTI system has impulse response $h[n] = (\frac{1}{4})^n U[n]$. Use DTFT to compute the output for each of the following inputs: (i) $x[n] = (\frac{3}{4})^n U[n]$ (ii) $x[n] = (n+1)(\frac{1}{4})^n U[n]$ (iii) $x[n] = (-1)^n$. (14) K_2

Module V

- 19(A) Compute the inverse Z transform of (7) K_3

$$H(z) = \frac{1}{\left(1 - \frac{1}{2}z^{-1}\right)\left(1 - \frac{1}{5}z^{-1}\right)}$$

for all possible ROCs

- 19(B) Compute the inverse Z transform of (7) K_3

$$H(z) = \cos(\alpha z^{-1})$$

for all possible ROCs

OR

- 20 Compute the Z -transform with ROC of
- i. $x[n] = \left(\frac{1}{3}\right)^n U[n]$ (4) K_3
 - ii. $x[n] = n\left(\frac{1}{3}\right)^n U[n]$ (5) K_3
 - iii. $x[n] = \sum_{i=-\infty}^n \left(\frac{1}{3}\right)^i U[i]$ (5) K_3

Estd.



2014

ECT 206	COMPUTER ARCHITECTURE AND MICROCONTROLLERS*	CATEGORY	L	T	P	CREDIT
		PCC	3	1	0	4

Preamble: This course aims to impart knowledge of basic computer architecture and modern microcontrollers.

Prerequisite: ECT203 Logic Circuit Design

Course Outcomes: After the completion of the course the student will be able to

CO 1	Explain the functional units, I/O and memory management w.r.t a typical computer architecture.
CO 2	Distinguish between microprocessor and microcontroller.
CO 3	Develop simple programs using assembly language programming.
CO 4	Interface 8051 microcontroller with peripheral devices using ALP/Embedded C
CO 5	Familiarize system software and Advanced RISC Machine Architecture.

Mapping of course outcomes with program outcomes

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
CO 1	3											3
CO 2	3											3
CO 3	3		3		3							3
CO 4	3	3	3		3							3
CO 5	3				3							3

Assessment Pattern

Bloom's Category	Continuous Assessment Tests		End Semester Examination
	1	2	
Remember	10	10	10
Understand	20	20	20
Apply	20	20	70
Analyse			
Evaluate			
Create			

Mark distribution

Total Marks	CIE	ESE	ESE Duration
150	50	100	3 hours

Continuous Internal Evaluation Pattern:

Attendance	: 10 marks
Continuous Assessment Test (2 numbers)	: 25 marks
Course project	: 15 marks

It is mandatory that a *course project* shall be undertaken by a student for this subject. The course project can be performed either as a hardware realization/simulation of a typical embedded system using Embedded C/ Assembly Language Programming. Instead of two assignments, two evaluations may be performed on the course project along with series tests, each carrying 5 marks. Upon successful completion of the project, a brief report shall be submitted by the student which shall be evaluated for 5 marks. The report has to be submitted for academic auditing. A few sample course projects are listed below:

Sample Course Projects

The below projects shall be done with the help of IDE for 8051/PIC/MSP/Arduino/Raspberry Pi-based interfacing boards/sensor modules.

1. Relay control
2. Distance measurement
3. Temperature measurement / Digital Thermometer
4. RF ID tags
5. Alphanumeric LCD display interface.
6. OLED display interfacing

End Semester Examination Pattern: There will be two parts; Part A and Part B. Part A contain 10 questions with 2 questions from each module, having 3 marks for each question. Students should answer all questions. Part B contains 2 questions from each module of which student should answer any one. Each question can have maximum 2 sub-divisions and carry 14 marks.

Course Level Assessment Questions

SYLLABUS

Module 1: Computer Arithmetic and Processor Basics

Algorithms for binary multiplication and division. Fixed and floating-point number representation. Functional units of a computer, Von Neumann and Harvard computer architectures, CISC and RISC architectures. Processor Architecture – General internal architecture, Address bus, Data bus, control bus. Register set – status register, accumulator, program counter, stack pointer, general purpose registers. Processor operation – instruction cycle, instruction fetch, instruction decode, instruction execute, timing response, instruction sequencing and execution (basic concepts, datapath).

Module 2: 8051 Architecture

Microcontrollers and Embedded Processors. Architecture – Block diagram of 8051, Pin configuration, Registers, Internal Memory, Timers, Port Structures, Interrupts. Assembly Language Programming - Addressing Modes, Instruction set (Detailed study of 8051 instruction set is required).

Module 3: Programming and Interfacing of 8051

Simple programming examples in assembly language. Interfacing with 8051 using Assembly language programming: LED, Seven segment LED display. Programming in C - Declaring variables, Simple examples – delay generation, port programming, code conversion.

Interfacing of – LCD display, Keyboard, Stepper Motor, DAC and ADC -- with 8051 and its programming.

Module 4: Advanced Concepts

8051 Timers/Counters - Modes and Applications. Serial Data Transfer – SFRs of serial port, working, Programming the 8051 to transfer data serially. Introduction to ARM - ARM family, ARM 7 register architecture. ARM programmer's model. System software - Assembler, Interpreter, Compiler, Linker, Loader, Debugger.

Module 5: The Memory System

Types of memory - RAM, ROM. Memory Characteristics and Hierarchy. Cache memory – The basics of Caches, Mapping techniques, Improving Cache performance. Virtual memory – Overlay, Memory management, Address translation. Input/Output Organization – Introduction, Synchronous vs. asynchronous I/O, Programmed I/O, Interrupt driven I/O, Direct Memory Access.

Text Books

1. Muhammed Ali Mazidi & Janice Gilli Mazidi, R.D. Kinley, The 8051 microcontroller and Embedded System, Pearson Education, 2nd edition.
2. Subrata Ghoshal, Computer Architecture and Organization: From 8085 to Core2Duo and beyond, Pearson, 2011.
3. Steve Furber, ARM System - on-chip Architecture, Pearson Education

Reference Books

1. Mano M M, Computer System Architecture, 3rd Ed, Prentice Hall of India.
2. Computer organization and design: The Hardware/Software interface/David A. Patterson, John L. Hennessy. — 5th ed.
3. Computer Organisation V. Carl Hamacher, Zvonko G. Vranesic, Safwat G.Zaky.
4. John P Hayes, Computer Architecture and Organization, McGraw Hill.
5. Ramesh S Goankar, 8085 Microprocessor Architecture, Applications and Programming, Penram International, 5/e.
6. The 8051 Microcontrollers: Architecture Programming and Applications, K Uma Rao & Andhe Pallavi, Pearson, 2011.
7. Stallings W., Computer Organisation and Architecture, 5/e, Pearson Education.

Course Contents and Lecture Schedule

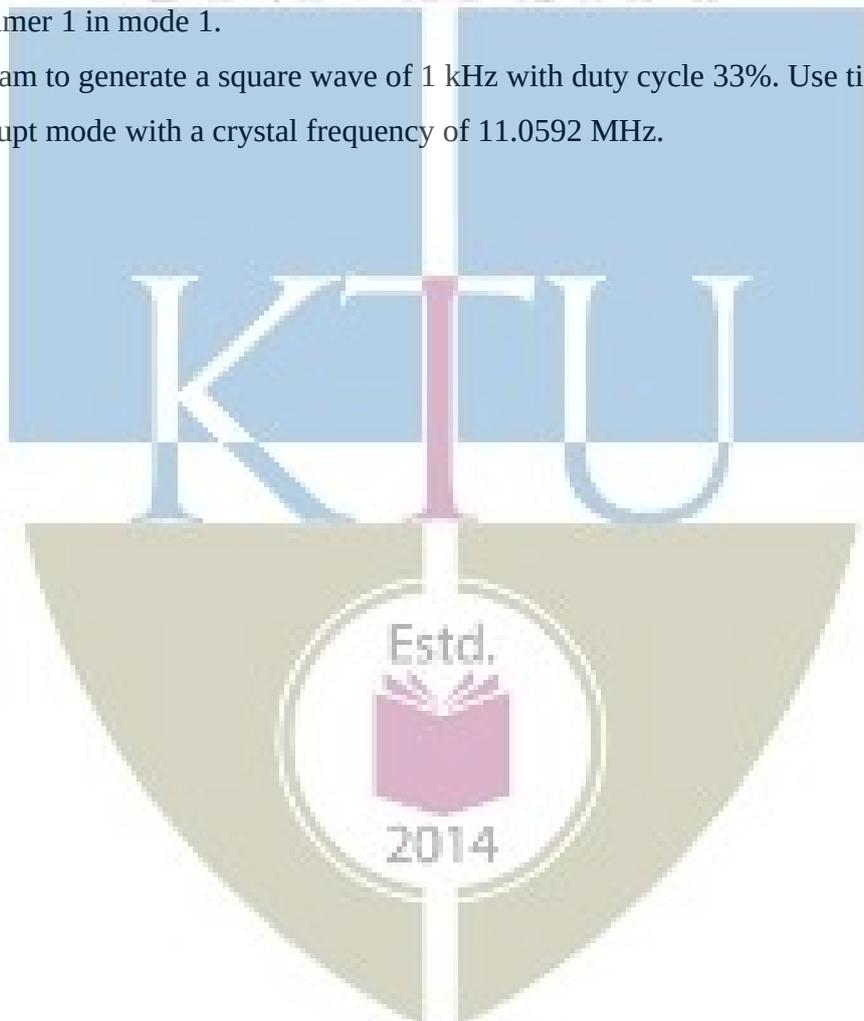
No	Topic	No. of Lectures
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1	Computer Arithmetic and Processor Basics	
1.1	Algorithms for binary multiplication and division	2
1.2	Fixed- and floating-point number representation in computers.	1
1.3	Functional units of a computer, Von Neumann and Harvard computer architectures, CISC and RISC architectures.	1
1.4	Processor Architecture – General internal architecture, Address bus, Data bus, control bus. Register set – status register, accumulator, program counter, stack pointer, general purpose registers.	2
1.5	Processor operation – instruction cycle, instruction fetch, instruction decode, instruction execute, timing response, instruction sequencing and execution (basic concepts), data path	3
2	8051 Architecture	
2.1	Microcontrollers and Embedded Processors and Applications	1
2.2	Architecture – Block diagram of 8051, Pin configuration, Registers, Internal Memory, Timers, Port Structures, Interrupts.	3
2.3	Addressing Modes of 8051	1
2.4	Instruction sets (Detailed study of 8051 instructions)	4
3	Programming and Interfacing of 8051	
3.1	Simple programming examples in assembly language.	2
3.2	Interfacing programming in Assembly language	2
3.3	Programming in C - Declaring variables, Simple examples – delay generation, port programming, code conversion.	3
3.4	Interfacing of 7 segment LCD display	1
3.5	Interfacing of Keyboard and stepper motor	2
3.6	Interfacing of DAC and ADC	2
4	Advanced Concepts	
4.1	8051 Timers/Counters - Modes and Applications	2
4.2	Serial Data Transfer – SFRs of serial port, working, Programming the 8051 to transfer data serially	2
4.3	Introduction to ARM - ARM family, ARM 7 register architecture. ARM programmer's model	2
4.4	System software - Assembler, Interpreter, Compiler, Linker, Loader, Debugger.	2
5	Memory System	
5.1	Types of memory - RAM, ROM. Memory Characteristics and Hierarchy	2
5.2	Cache memory – The basics of Caches, Mapping techniques, Improving Cache performance	2
5.3	Virtual memory – Overlay, Memory management, Address translation	2
5.4	Input/Output Organization – Introduction, Synchronous vs. asynchronous I/O, Programmed I/O, Interrupt driven I/O, Direct Memory Access.	3

Simulation assignments

The following examples may be solved in C program

1. Program to convert the ASCII number into unpacked BCD.
2. Program to swap a number $0x\ ab$ to $0x\ ba$, where a and b are hex digits.
3. Program to find the number of 1's in an 8-bit data item.
4. Program to display 'M' and 'E' on the LCD connected to 8051 using the BUSY FLAG.
5. Program to rotate a stepper motor 50° in the clock wise direction.
6. Program to toggle pin P1.4 every second using interrupts for a frequency of 22 MHz. Use timer 1 in mode 1.
7. Program to generate a square wave of 1 kHz with duty cycle 33%. Use timer 1 in interrupt mode with a crystal frequency of 11.0592 MHz.



A P J Abdul Kalam Technological University
Fourth Semester B Tech Degree Examination
Branch: Electronics and Communication

Course: ECT 206 COMPUTER ARCHITECTURE AND MICROCONTROLLERS

Time: 3 Hrs

Max. Marks: 100

Part – A

Answer all questions. Questions carry **3 marks** each.

1. Represent 4946.278941 as a 32 bit number in IEEE 754 format.
2. Which is more important for the functioning of a basic processor, Program Counter or Stack Pointer. Justify your answer.
3. List the components of 8051 microcontroller.
4. Write the operations happening in the following instructions:
ADD A, 56
XCHD A, @R1
DJNZ R6, LABEL
DIV AB
XRL A, #0FFh
JB P1.2 LABEL
5. Write an embedded C program for 8051 microcontroller to continuously rotate a stepper motor clockwise.
6. Write an embedded C program for 8051 microcontroller to blink P2.5 every 2 seconds
7. List the different modes and give corresponding uses of timers in 8051 microcontroller
8. Which are the SFRs used for serial communication in 8051 microcontroller. Give there functions.
9. Illustrate the memory hierarchy in a computer system.
10. Is ROM a random access memory? Justify your answer.

Answer one question each from all modules

Module – 1

11. a) With an example explain the “shift and add” algorithm for multiplying two binary numbers. (5 marks)
 b) With relevant diagrams illustrate the functioning of a basic (non – pipelined) processor. (9 marks)

OR

12. a) Differentiate RISC and CISC architectures. (4 marks)
 b) Explain Instruction Cycle with a sample timing diagram (10 marks)

Module – 2

13. a) Illustrate the complete memory organisation of 8051 microcontroller (10 marks)
 b) Differentiate microprocessors and microcontrollers. (4 marks)

OR

14. a) Explain about the Addressing Modes of 8051 microcontroller with examples. (7 marks)
 b) Describe the classification of the Instruction Set of 8051 microcontroller with examples. (7 marks)

Module – 3

15. a) Write an embedded C program for 8051 microcontroller to read an analogue signal from an ADC and reproduce the same using a DAC (9 marks)
 b) Write an assembly language program for 8051 microcontroller to sort N number in ascending order. Assume that the numbers are stored in continuous locations starting from 0x4321 onwards. (5 marks)

OR

16. a) Write an embedded C program for 8051 microcontroller to repeatedly display the sequence 1,5,8,0,2,6,4,9,3,7 using a 7 – segment display with a delay of 1.5 seconds between each number. (9 marks)
 b) Write an assembly language program for 8051 microcontroller to find the cube of an 8 – bit number (5 marks)

Module – 4

17. a) Assume a switch is connected to pin PL7. Write a embedded C program for 8051 microcontroller to monitor its status and send two messages to serial port continuously as follows:
 SW=0 send “NO”
 SW=1 send “YES”
 Assume XTAL = 11.0592 MHz, 9600 baud, 8-bit data, and 1 stop bit. (10 marks)
 b) Describe the ARM 7 register architecture (4 marks)

OR

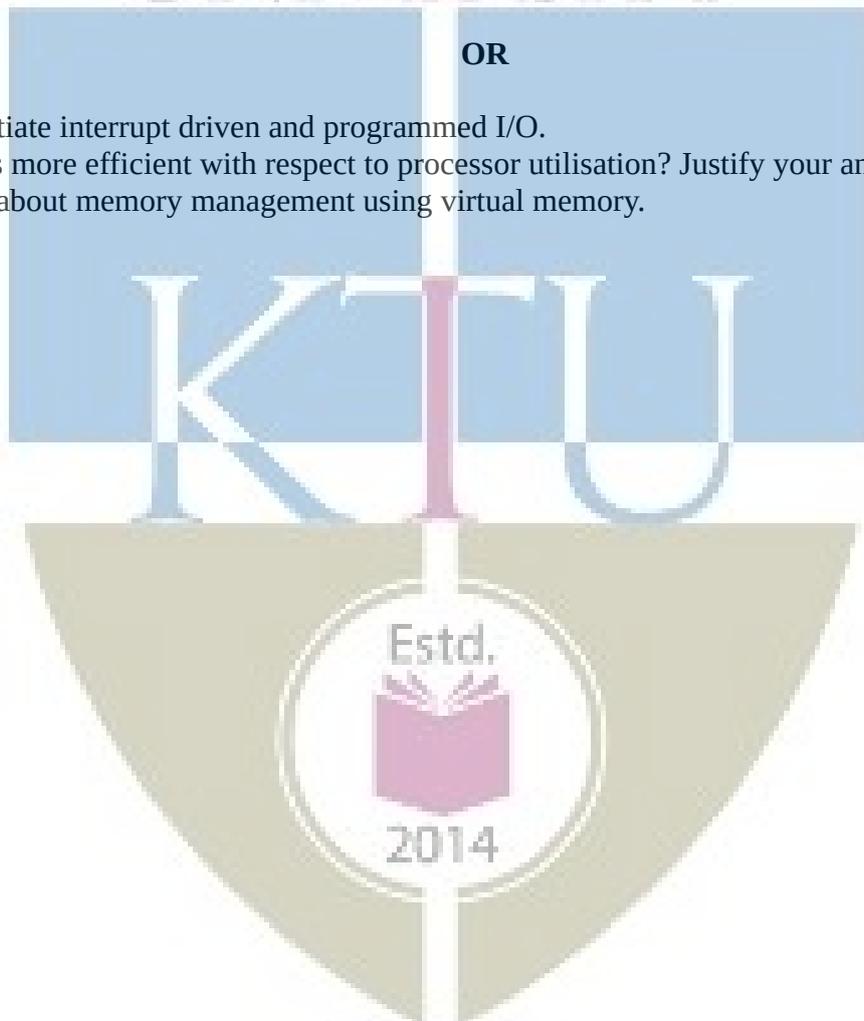
18. a) Write an embedded C program for 8051 microcontroller to send the message "Hello World!" to serial port. Assume a SW is connected to pin P1.2. Monitor its status and set the baud rate as follows:
SW = 0, 4800 baud rate
SW = 1, 9600 baud rate
Assume XTAL = 11.0592 Mhz, 8-bit data, and 1 stop bit (10 marks)
- b) Explain how a HLL program is executed as machine language in a processor (4 marks)

Module – 5

19. a) Differentiate synchronous and asynchronous I/O.
Which is more efficient with respect to processor utilisation? Justify your answer (8 marks)
- b) Explain direct mapping of cache memory with an example (6 marks)

OR

20. a) Differentiate interrupt driven and programmed I/O.
Which is more efficient with respect to processor utilisation? Justify your answer (8 marks)
- b) Explain about memory management using virtual memory. (6 marks)



CODE	COURSE NAME	CATEGORY	L	T	P	CREDIT
				2	0	0
EST 200	DESIGN AND ENGINEERING					

Preamble:

The purpose of this course is to

- i) introduce the undergraduate engineering students the fundamental principles of design engineering,
- ii) make them understand the steps involved in the design process and
- iii) familiarize them with the basic tools used and approaches in design.

Students are expected to apply design thinking in learning as well as while practicing engineering, which is very important and relevant for today. Case studies from various practical situations will help the students realize that design is not only concerned about the function but also many other factors like customer requirements, economics, reliability, etc. along with a variety of life cycle issues.

The course will help students to consider aesthetics, ergonomics and sustainability factors in designs and also to practice professional ethics while designing.

Prerequisite:

Nil. The course will be generic to all engineering disciplines and will not require specialized preparation or prerequisites in any of the individual engineering disciplines.

Course Outcomes:

After the completion of the course the student will be able to

CO 1	Explain the different concepts and principles involved in design engineering.
CO 2	Apply design thinking while learning and practicing engineering.
CO 3	Develop innovative, reliable, sustainable and economically viable designs incorporating knowledge in engineering.

Mapping of course outcomes with program outcomes

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
CO 1	2	1					1			1		
CO 2		2				1		1				2
CO 3			2			1	1		2	2		1

Assessment Pattern**Continuous Internal Evaluation (CIE) Pattern:**

Attendance	: 10 marks
Continuous Assessment Test (2 numbers)	: 25 marks
Assignment/Quiz/Course project	: 15 marks

End Semester Examination (ESE) Pattern: There will be two parts; Part A and Part B.

Part A : 30 marks

part B : 70 marks

Part A contains 10 questions with 2 questions from each module, having 3 marks for each question. Students should answer all questions.

Part B contains 2 case study questions from each module of which student should answer any one. Each question carry 14 marks and can have maximum 2 sub questions.

Mark distribution

Total Marks	CIE	ESE	ESE Duration
150	50	100	3 hours

Bloom's Category	Continuous Assessment Tests		End Semester Examination
	1	2	
Remember	5	5	10
Understand	10	10	20
Apply	35	35	70
Analyse	-	-	-
Evaluate	-	-	-
Create	-	-	-

Course Level Assessment Questions

Course Outcome 1 (CO1): Appreciate the different concepts and principles involved in design engineering.

1. State how engineering design is different from other kinds of design
2. List the different stages in a design process.
3. Describe design thinking.
4. State the function of prototyping and proofing in engineering design.
5. Write notes on the following concepts in connection with design engineering 1) Modular Design, 2) Life Cycle Design, 3) Value Engineering, 4) Concurrent Engineering, and 5) Reverse Engineering
6. State design rights.

Course Outcome 2 (CO2) Apply design thinking while learning and practicing engineering.

1. Construct the iterative process for design thinking in developing simple products like a pen, umbrella, bag, etc.
2. Show with an example how divergent-convergent thinking helps in generating alternative designs and then how to narrow down to the best design.
3. Describe how a problem-based learning helps in creating better design engineering solutions.
4. Discuss as an engineer, how ethics play a decisive role in your designs

Course Outcome 3 (CO3): Develop innovative, reliable, sustainable and economically viable designs incorporating different segments of knowledge in engineering.

1. Illustrate the development of any simple product by passing through the different stages of design process
2. Show the graphical design communication with the help of detailed 2D or 3D drawings for any simple product.
3. Describe how to develop new designs for simple products through bio-mimicry.

Model Question paper

Page 1 of 2

Reg No.: _____ Name: _____

**APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY
THIRD/FOURTH SEMESTER B.TECH DEGREE EXAMINATION**

Course Code: EST 200

Course Name: DESIGN AND ENGINEERING

Max. Marks: 100 Duration: 3 Hours

PART A**Answer all questions, each question carries 3 marks****Use only hand sketches**

- (1) Write about the basic design process.
- (2) Describe how to finalize the design objectives.
- (3) State the role of divergent-convergent questioning in design thinking.
- (4) Discuss how to perform design thinking in a team managing the conflicts.
- (5) Show how engineering sketches and drawings convey designs.
- (6) Explain the role of mathematics and physics in design engineering process.
- (7) Distinguish between project-based learning and problem-based learning in design engineering.
- (8) Describe how concepts like value engineering, concurrent engineering and reverse engineering influence engineering designs?
- (9) Show how designs are varied based on the aspects of production methods, life span, reliability and environment?
- (10) Explain how economics influence the engineering designs?

(10x3 marks =30 marks)**Part B****Answer any ONE question from each module. Each question carry 14 marks****Module 1**

- (11) Show the designing of a wrist watch going through the various stages of the design process. Use hand sketches to illustrate the processes.
- or**
- (12) Find the customer requirements for designing a new car showroom. Show how the design objectives were finalized considering the design constraints?

Module 2

(13) Illustrate the design thinking approach for designing a bag for college students within a limited budget. Describe each stage of the process and the iterative procedure involved. Use hand sketches to support your arguments.

or

(14) Construct a number of possible designs and then refine them to narrow down to the best design for a drug trolley used in hospitals. Show how the divergent-convergent thinking helps in the process. Provide your rationale for each step by using hand sketches only.

Module 3

(15) Graphically communicate the design of a thermo flask used to keep hot coffee. Draw the detailed 2D drawings of the same with design detailing, material selection, scale drawings, dimensions, tolerances, etc. Use only hand sketches.

or

(16) Describe the role of mathematical modelling in design engineering. Show how mathematics and physics play a role in designing a lifting mechanism to raise 100 kg of weight to a floor at a height of 10 meters in a construction site.

Module 4

(17) Show the development of a nature inspired design for a solar powered bus waiting shed beside a highway. Relate between natural and man-made designs. Use hand sketches to support your arguments.

or

(18) Show the design of a simple sofa and then depict how the design changes when considering 1) aesthetics and 2) ergonomics into consideration. Give hand sketches and explanations to justify the changes in designs.

Module 5

(19) Examine the changes in the design of a foot wear with constraints of 1) production methods, 2) life span requirement, 3) reliability issues and 4) environmental factors. Use hand sketches and give proper rationalization for the changes in design.

or

(20) Describe how to estimate the cost of a particular design using ANY of the following: i) a website, ii) the layout of a plant, iii) the elevation of a building, iv) an electrical or electronic system or device and v) a car.

Show how economics will influence the engineering designs. Use hand sketches to support your arguments.

(5x14 marks =70 marks)

Syllabus

Module 1

Design Process:- Introduction to Design and Engineering Design, Defining a Design Process:-Detailing Customer Requirements, Setting Design Objectives, Identifying Constraints, Establishing Functions, Generating Design Alternatives and Choosing a Design.

Module 2

Design Thinking Approach:-Introduction to Design Thinking, Iterative Design Thinking Process Stages: Empathize, Define, Ideate, Prototype and Test. Design Thinking as Divergent-Convergent Questioning. Design Thinking in a Team Environment.

Module 3

Design Communication (Languages of Engineering Design):-Communicating Designs Graphically, Communicating Designs Orally and in Writing. Mathematical Modeling In Design, Prototyping and Proofing the Design.

Module 4

Design Engineering Concepts:-Project-based Learning and Problem-based Learning in Design.Modular Design and Life Cycle Design Approaches. Application of Biomimicry,Aesthetics and Ergonomics in Design. Value Engineering, Concurrent Engineering, and Reverse Engineering in Design.

Module 5

Expediency, Economics and Environment in Design Engineering:-Design for Production, Use, and Sustainability. Engineering Economics in Design. Design Rights. Ethics in Design

Text Books

- 1) YousefHaik, SangarappillaiSivaloganathan, Tamer M. Shahin, Engineering Design Process, Cengage Learning 2003, Third Edition, ISBN-10: 9781305253285,
- 2) Voland, G., Engineering by Design, Pearson India 2014, Second Edition, ISBN 9332535051

Reference Books

- 1.Philip Kosky, Robert Balmer, William Keat, George Wise, Exploring Engineering, Fourth Edition: An Introduction to Engineering and Design, Academic Press 2015, 4th Edition, ISBN: 9780128012420.
2. Clive L. Dym, Engineering Design: A Project-Based Introduction, John Wiley & Sons, New York 2009, Fourth Edition, ISBN: 978-1-118-32458-5
3. Nigel Cross, Design Thinking: Understanding How Designers Think and Work, Berg Publishers 2011, First Edition, ISBN: 978-1847886361
4. Pahl, G., Beitz, W., Feldhusen, J., Grote, K.-H., Engineering Design: A Systematic Approach, Springer 2007, Third Edition, ISBN 978-1-84628-319-2

Course Contents and Lecture Schedule

No	Topic	No. of Lectures
1	<u>Module 1: Design Process</u>	
1.1	Introduction to Design and Engineering Design. <i>What does it mean to design something? How Is engineering design different from other kinds of design? Where and when do engineers design? What are the basic vocabulary in engineering design? How to learn and do engineering design.</i>	1
1.2	<i>Defining a Design Process-: Detailing Customer Requirements.</i> <i>How to do engineering design? Illustrate the process with an example. How to identify the customer requirements of design?</i>	1
1.3	<i>Defining a Design Process-: Setting Design Objectives, Identifying Constraints, Establishing Functions.</i> <i>How to finalize the design objectives? How to identify the design constraints? How to express the functions a design in engineering terms?</i>	1
1.4	<i>Defining a Design Process-: Generating Design Alternatives and Choosing a Design.</i> <i>How to generate or create feasible design alternatives? How to identify the "best possible design"?</i>	1
1.5	Case Studies:- Stages of Design Process. <i>Conduct exercises for designing simple products going through the different stages of design process.</i>	1
2	<u>Module 2: Design Thinking Approach</u>	
2.1	Introduction to Design Thinking <i>How does the design thinking approach help engineers in creating innovative and efficient designs?</i>	1
2.2	Iterative Design Thinking Process Stages: Empathize, Define, Ideate, Prototype and Test. <i>How can the engineers arrive at better designs utilizing the iterative design thinking process (in which knowledge acquired in the later stages can be applied back to the earlier stages)?</i>	1
2.3	Design Thinking as Divergent-Convergent Questioning. <i>Describe how to create a number of possible designs and then how to refine and narrow down to the 'best design'.</i>	1
2.4	Design Thinking in a Team Environment. <i>How to perform design thinking as a team managing the conflicts ?</i>	1
2.5	Case Studies: Design Thinking Approach. <i>Conduct exercises using the design thinking approach for</i>	1

	<i>designing any simple products within a limited time and budget</i>	
3	<u>Module 3: Design Communication (Languages of Engineering Design)</u>	
3.1	Communicating Designs Graphically. <i>How do engineering sketches and drawings convey designs?</i>	1
3.2	Communicating Designs Orally and in Writing. <i>How can a design be communicated through oral presentation or technical reports efficiently?</i>	1
First Series Examination		
3.3	Mathematical Modelling in Design. <i>How do mathematics and physics become a part of the design process?</i>	1
3.4	Prototyping and Proofing the Design. <i>How to predict whether the design will function well or not?</i>	1
3.5	Case Studies: Communicating Designs Graphically. <i>Conduct exercises for design communication through detailed 2D or 3D drawings of simple products with design detailing, material selection, scale drawings, dimensions, tolerances, etc.</i>	1
4	<u>Module 4: Design Engineering Concepts</u>	
4.1	Project-based Learning and Problem-based Learning in Design. <i>How engineering students can learn design engineering through projects?</i> <i>How students can take up problems to learn design engineering?</i>	1
4.2	Modular Design and Life Cycle Design Approaches. <i>What is modular approach in design engineering? How it helps?</i> <i>How the life cycle design approach influences design decisions?</i>	1
4.3	Application of Bio-mimicry, Aesthetics and Ergonomics in Design. <i>How do aesthetics and ergonomics change engineering designs?</i> <i>How do the intelligence in nature inspire engineering designs? What are the common examples of bio-mimicry in engineering?</i>	1
4.4	Value Engineering, Concurrent Engineering, and Reverse Engineering in Design. <i>How do concepts like value engineering , concurrent engineering and reverse engineering influence engineering designs?</i>	1
4.5	Case Studies: Bio-mimicry based Designs. <i>Conduct exercises to develop new designs for simple</i>	1

	<i>products using bio-mimicry and train students to bring out new nature inspired designs.</i>	
5	<u>Module 5: Expediency, Economics and Environment in Design Engineering</u>	
5.1	Design for Production, Use, and Sustainability. <i>How designs are finalized based on the aspects of production methods, life span, reliability and environment?</i>	1
5.2	Engineering Economics in Design. <i>How to estimate the cost of a particular design and how will economics influence the engineering designs?</i>	1
5.3	Design Rights. <i>What are design rights and how can an engineer put it into practice?</i>	1
5.4	Ethics in Design. <i>How do ethics play a decisive role in engineering design?</i>	1
5.5	Case Studies: Design for Production, Use, and Sustainability. <i>Conduct exercises using simple products to show how designs change with constraints of production methods, life span requirement, reliability issues and environmental factors.</i>	1
Second Series Examination		



Code.	Course Name	L	T	P	Hrs	Credit
HUT 200	Professional Ethics	2	0	0	2	2

Preamble: To enable students to create awareness on ethics and human values.

Prerequisite: Nil

Course Outcomes: After the completion of the course the student will be able to

CO 1	Understand the core values that shape the ethical behaviour of a professional.
CO 2	Adopt a good character and follow an ethical life.
CO 3	Explain the role and responsibility in technological development by keeping personal ethics and legal ethics.
CO 4	Solve moral and ethical problems through exploration and assessment by established experiments.
CO 5	Apply the knowledge of human values and social values to contemporary ethical values and global issues.

Mapping of course outcomes with program outcomes

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO1 0	PO1 1	PO1 2
CO 1								2			2	
CO 2								2			2	
CO 3								3			2	
CO 4								3			2	
CO 5								3			2	

Assessment Pattern

Bloom's category	Continuous Assessment Tests		End Semester Exam
	1	2	
Remember	15	15	30
Understood	20	20	40
Apply	15	15	30

Mark distribution

Total Marks	CIE	ESE	ESE Duration
150	50	100	3 hours

Continuous Internal Evaluation Pattern:

Attendance	: 10 marks
Continuous Assessment Tests (2 Nos)	: 25 marks
Assignments/Quiz	: 15 marks

End Semester Examination Pattern: There will be two parts; Part A and Part B. Part A contains 10 questions with 2 questions from each module, having 3 marks for each question. Students should answer all questions. Part B contains 2 questions from each module of which student should answer any one. Each question can have maximum 2 sub-divisions and carry 14 marks.

Course Level Assessment Questions**Course Outcome 1 (CO1):**

1. Define integrity and point out ethical values.
2. Describe the qualities required to live a peaceful life.
3. Explain the role of engineers in modern society.

Course Outcome 2 (CO2)

1. Derive the codes of ethics.
2. Differentiate consensus and controversy.
3. Discuss in detail about character and confidence.

Course Outcome 3(CO3):

1. Explain the role of professional's ethics in technological development.
2. Distinguish between self interest and conflicts of interest.
3. Review on industrial standards and legal ethics.

Course Outcome 4 (CO4):

1. Illustrate the role of engineers as experimenters.
2. Interpret the terms safety and risk.
3. Show how the occupational crimes are resolved by keeping the rights of employees.

Course Outcome 5 (CO5):

1. Exemplify the engineers as managers.
2. Investigate the causes and effects of acid rain with a case study.
3. Explore the need of environmental ethics in technological development.

Model Question paper

QP CODE:

Reg No: _____

PAGES:3

Name : _____

**APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY THIRD/FOURTH SEMESTER
B.TECH DEGREE EXAMINATION, MONTH & YEAR**

Course Code: HUT 200

Course Name: PROFESSIONAL ETHICS

Max. Marks: 100

Duration: 3 Hours

(2019-Scheme)

PART A**(Answer all questions, each question carries 3 marks)**

1. Define empathy and honesty.
2. Briefly explain about morals, values and ethics.
3. Interpret the two forms of self-respect.
4. List out the models of professional roles.
5. Indicate the advantages of using standards.
6. Point out the conditions required to define a valid consent?
7. Identify the conflicts of interests with an example?
8. Recall confidentiality.
9. Conclude the features of biometric ethics.
10. Name any three professional societies and their role relevant to engineers.

(10x3 = 30 marks)

PART B**(Answer one full question from each module, each question carries 14 marks)****MODULE I****11. a)** Classify the relationship between ethical values and law?**b)** Compare between caring and sharing.

(10+4 = 14 marks)

Or**12. a)** Exemplify a comprehensive review about integrity and respect for others.

b) Discuss about co-operation and commitment.

(8+6 = 14 marks)

MODULE II

13.a) Explain the three main levels of moral developments, devised by Kohlberg.

b) Differentiate moral codes and optimal codes.

(10+4 = 14 marks)

Or

14. a) Extrapolate the duty ethics and right ethics.

b) Discuss in detail the three types of inquiries in engineering ethics

(8+6 = 14 marks)

MODULE III

15.a) Summarize the following features of morally responsible engineers.

(i) Moral autonomy

(ii) Accountability

b) Explain the rights of employees

(8+6 = 14 marks)

Or

16. a) Explain the reasons for Chernobyl mishap ?

b) Describe the methods to improve collegiality and loyalty.

(8+6 = 14 marks)

MODULE IV

17.a) Execute collegiality with respect to commitment, respect and connectedness.

b) Identify conflicts of interests with an example.

(8+6 = 14 marks)

Or

18. a) Explain in detail about professional rights and employee rights.

b) Exemplify engineers as managers.

MODULE V

19.a) Evaluate the technology transfer and appropriate technology.

b) Explain about computer and internet ethics.

(8+6 = 14 marks)

Or

20. a) Investigate the causes and effects of acid rain with a case study.

b) Conclude the features of ecocentric and biocentric ethics.

(8+6 = 14 marks)

Syllabus

Module 1 – Human Values.

Morals, values and Ethics – Integrity- Academic integrity-Work Ethics- Service Learning- Civic Virtue- Respect for others- Living peacefully- Caring and Sharing- Honestly- courage-Cooperation commitment- Empathy-Self Confidence -Social Expectations.

Module 2 - Engineering Ethics & Professionalism.

Senses of Engineering Ethics - Variety of moral issues- Types of inquiry- Moral dilemmas –Moral Autonomy – Kohlberg’s theory- Gilligan’s theory- Consensus and Controversy-Profession and Professionalism- Models of professional roles-Theories about right action –Self interest-Customs and Religion- Uses of Ethical Theories.

Module 3- Engineering as social Experimentation.

Engineering as Experimentation – Engineers as responsible Experimenters- Codes of Ethics- Plagiarism- A balanced outlook on law - Challenges case study- Bhopal gas tragedy.

Module 4- Responsibilities and Rights.

Collegiality and loyalty – Managing conflict- Respect for authority- Collective bargaining- Confidentiality- Role of confidentiality in moral integrity-Conflicts of interest- Occupational crime- Professional rights- Employee right- IPR Discrimination.

Module 5- Global Ethical Issues.

Multinational Corporations- Environmental Ethics- Business Ethics- Computer Ethics -Role in Technological Development-Engineers as Managers- Consulting Engineers- Engineers as Expert witnesses and advisors-Moral leadership.

Text Book

1. M Govindarajan, S Natarajan and V S Senthil Kumar, Engineering Ethics, PHI Learning Private Ltd, New Delhi,2012.
2. R S Naagarazan, A text book on professional ethics and human values, New age international (P) limited ,New Delhi,2006.

Reference Books

1. Mike W Martin and Roland Schinzinger, Ethics in Engineering,4th edition, Tata McGraw Hill Publishing Company Pvt Ltd, New Delhi,2014.
2. Charles D Fleddermann, Engineering Ethics, Pearson Education/ Prentice Hall of India, New Jersey,2004.
3. Charles E Harris, Michael S Protchard and Michael J Rabins, Engineering Ethics- Concepts and cases, Wadsworth Thompson Learning, United states,2005.
4. <http://www.slideword.org/slidestag.aspx/human-values-and-Professional-ethics>.

Course Contents and Lecture Schedule

SL.No	Topic	No. of Lectures 25
1	Module 1 – Human Values.	
1.1	Morals, values and Ethics, Integrity, Academic Integrity, Work Ethics	1
1.2	Service Learning, Civic Virtue, Respect for others, Living peacefully	1
1.3	Caring and Sharing, Honesty, Courage, Co-operation commitment	2
1.4	Empathy, Self Confidence, Social Expectations	1
2	Module 2- Engineering Ethics & Professionalism.	
2.1	Senses of Engineering Ethics, Variety of moral issues, Types of inquiry	1
2.2	Moral dilemmas, Moral Autonomy, Kohlberg's theory	1
2.3	Gilligan's theory, Consensus and Controversy, Profession & Professionalism, Models of professional roles, Theories about right action	2
2.4	Self interest-Customs and Religion, Uses of Ethical Theories	1
3	Module 3- Engineering as social Experimentation.	
3.1	Engineering as Experimentation, Engineers as responsible Experimenters	1
3.2	Codes of Ethics, Plagiarism, A balanced outlook on law	2
3.3	Challenger case study, Bhopal gas tragedy	2
4	Module 4- Responsibilities and Rights.	
4.1	Collegiality and loyalty, Managing conflict, Respect for authority	1
4.2	Collective bargaining, Confidentiality, Role of confidentiality in moral integrity, Conflicts of interest	2
4.3	Occupational crime, Professional rights, Employee right, IPR Discrimination	2
5	Module 5- Global Ethical Issues.	
5.1	Multinational Corporations, Environmental Ethics, Business Ethics, Computer Ethics	2
5.2	Role in Technological Development, Moral leadership	1
5.3	Engineers as Managers, Consulting Engineers, Engineers as Expert witnesses and advisors	2

CODE MCN202	COURSE NAME CONSTITUTION OF INDIA	CATEGORY	L	T	P	CREDIT
			2	0	0	NIL

Preamble:

The study of their own country constitution and studying the importance environment as well as understanding their own human rights help the students to concentrate on their day to day discipline. It also gives the knowledge and strength to face the society and people.

Prerequisite: Nil

Course Outcomes: After the completion of the course the student will be able to

CO 1	Explain the background of the present constitution of India and features.
CO 2	Utilize the fundamental rights and duties.
CO 3	Understand the working of the union executive, parliament and judiciary.
CO 4	Understand the working of the state executive, legislature and judiciary.
CO 5	Utilize the special provisions and statutory institutions.
CO 6	Show national and patriotic spirit as responsible citizens of the country

Mapping of course outcomes with program outcomes

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
CO 1						2	2	2		2		
CO 2						3	3	3		3		
CO 3						3	2	3		3		
CO 4						3	2	3		3		
CO 5						3	2	3		3		
CO 6						3	3	3		2		

Assessment Pattern

Bloom's Category	Continuous Assessment Tests		End Semester Examination
	1	2	
Remember	20	20	40
Understand	20	20	40
Apply	10	10	20
Analyse			

Evaluate			
Create			

Mark distribution

Total Marks	CIE	ESE	ESE Duration
150	50	100	3 hours

Continuous Internal Evaluation Pattern:

Attendance	: 10 marks
Continuous Assessment Test (2 numbers)	: 25 marks
Assignment/Quiz/Course project	: 15 marks

End Semester Examination Pattern: There will be two parts; Part A and Part B. Part A contain 10 questions with 2 questions from each module, having 3 marks for each question. Students should answer all questions. Part B contains 2 questions from each module of which student should answer any one. Each question can have maximum 2 sub-divisions and carry 14 marks.

Course Level Assessment Questions**Course Outcome 1 (CO1):**

- 1 Discuss the historical background of the Indian constitution.
- 2 Explain the salient features of the Indian constitution.
- 3 Discuss the importance of preamble in the implementation of constitution.

Course Outcome 2 (CO2)

- 1 What are fundamental rights ? Examine each of them.
- 2 Examine the scope of freedom of speech and expression underlying the constitution.
- 3 The thumb impression of an accused is taken by the police against his will. He contends that this is a violation of his rights under Art 20(3) of the constitution. Decide.

Course Outcome 3(CO3):

- 1 Explain the powers of the President to suspend the fundamental rights during emergency.

- 2 Explain the salient features of appeal by special leave.
3. List the constitutional powers of President.

Course Outcome 4 (CO4):

- 1 Discuss the constitutional powers of Governor.
- 2 Examine the writ jurisdiction of High court.
- 3 Discuss the qualification and disqualification of membership of state legislature.

Course Outcome 5 (CO5):

- 1 Discuss the duties and powers of comptroller of auditor general.
- 2 Discuss the proclamation of emergency.
- 3 A state levies tax on motor vehicles used in the state, for the purpose of maintaining roads in the state. X challenges the levy of the tax on the ground that it violates the freedom of interstate commerce guaranteed under Art 301. Decide.

Course Outcome 6 (CO6):

- 1 Explain the advantages of citizenship.
- 2 List the important principles contained in the directive principles of state policy.
- 3 Discuss the various aspects contained in the preamble of the constitution

Model Question paper**PART A**

(Answer all questions. Each question carries 3 marks)

- 1 Define and explain the term constitution.
- 2 Explain the need and importance of Preamble.
- 3 What is directive principle of state policy?
- 4 Define the State.
- 5 List the functions of Attorney general of India.

- 6 Explain the review power of Supreme court.
- 7 List the qualifications of Governor.
- 8 Explain the term and removal of Judges in High court.
- 9 Explain the powers of public service commission.
- 10 List three types of emergency under Indian constitution.

(10X3=30marks)

PART B

(Answer on question from each module. Each question carries 14 marks)

Module 1

- 11 Discuss the various methods of acquiring Indian citizenship.
- 12 Examine the salient features of the Indian constitution.

Module 2

- 13 A high court passes a judgement against X. X desires to file a writ petition in the supreme court under Art32, on the ground that the judgement violates his fundamental rights. Advise him whether he can do so.
- 14 What is meant by directive principles of State policy? List the directives.

Module3

- 15 Describe the procedure of election and removal of the President of India.
- 16 Supreme court may in its discretion grant special leave to appeal. Examine the situation.

Module 4

- 17 Discuss the powers of Governor.
- 18 X filed a writ petition under Art 226 which was dismissed. Subsequently, he filed a writ petition under Art 32 of the constitution, seeking the same remedy. The Government argued that the writ petition should be dismissed, on the ground of res judicata. Decide.

Module 5

19 Examine the scope of the financial relations between the union and the states.

20 Discuss the effects of proclamation of emergency.

(14X5=70marks)

Syllabus

Module 1 Definition, historical back ground, features, preamble, territory, citizenship.

Module 2 State, fundamental rights, directive principles, duties.

Module 3 The machinery of the union government.

Module 4 Government machinery in the states

Module 5 The federal system, Statutory Institutions, miscellaneous provisions.

Text Books

1 D D Basu, Introduction to the constitution of India, Lexis Nexis, New Delhi, 24e, 2019

2 PM Bhakshi, The constitution of India, Universal Law, 14e, 2017

Reference Books

1 Ministry of law and justice, The constitution of India, Govt of India, New Delhi, 2019.

2 JN Pandey, The constitutional law of India, Central Law agency, Allahabad, 51e, 2019

3 MV Pylee, India's Constitution, S Chand and company, New Delhi, 16e, 2016

Course Contents and Lecture Schedule

No	Topic	No. of Lectures
1	Module 1	
1.1	Definition of constitution, historical back ground, salient features of the constitution.	1
1.2	Preamble of the constitution, union and its territory.	1
1.3	Meaning of citizenship, types, termination of citizenship.	2
2	Module 2	
2.1	Definition of state, fundamental rights, general nature, classification, right to equality ,right to freedom , right against exploitation	2

2.2	Right to freedom of religion, cultural and educational rights, right to constitutional remedies. Protection in respect of conviction for offences.	2
2.3	Directive principles of state policy, classification of directives, fundamental duties.	2
3	Module 3	
3.1	The Union executive, the President, the vice President, the council of ministers, the Prime minister, Attorney-General, functions.	2
3.2	The parliament, composition, Rajya sabha, Lok sabha, qualification and disqualification of membership, functions of parliament.	2
3.3	Union judiciary, the supreme court, jurisdiction, appeal by special leave.	1
4	Module 4	
4.1	The State executive, the Governor, the council of ministers, the Chief minister, advocate general, union Territories.	2
4.2	The State Legislature, composition, qualification and disqualification of membership, functions.	2
4.3	The state judiciary, the high court, jurisdiction, writs jurisdiction.	1
5	Module 5	
5.1	Relations between the Union and the States, legislative relation, administrative relation, financial Relations, Inter State council, finance commission.	1
5.2	Emergency provision, freedom of trade commerce and inter course, comptroller and auditor general of India, public Services, public service commission, administrative Tribunals.	2
5.3	Official language, elections, special provisions relating to certain classes, amendment of the Constitution.	2

ECL 202	ANALOG CIRCUITS AND SIMULATION LAB	CATEGORY	L	T	P	CREDIT
		PCC	0	0	3	2

Preamble: This course aims to

- (i) familiarize students with the Analog Circuits Design through the implementation of basic Analog Circuits using discrete components.
- (ii) familiarize students with simulation of basic Analog Circuits.

Prerequisite: Nil

Course Outcomes: After the completion of the course the student will be able to

CO 1	Design and demonstrate the functioning of basic analog circuits using discrete components.
CO 2	Design and simulate the functioning of basic analog circuits using simulation tools.
CO 3	Function effectively as an individual and in a team to accomplish the given task.

Mapping of course outcomes with program outcomes

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
CO 1	3	3	3						2			2
CO 2	3	3	3		3				2			2
CO 3	3	3	3						3			3

Assessment

Mark distribution

Total Marks	CIE	ESE	ESE Duration
150	75	75	2.5 hours

Continuous Internal Evaluation Pattern:

Attendance	:	15 marks
Continuous Assessment	:	30 marks
Internal Test (Immediately before the second series test)	:	30 marks

End Semester Examination Pattern: The following guidelines should be followed regarding award of marks

- | | |
|--|------------|
| (a) Preliminary work | : 15 Marks |
| (b) Implementing the work/Conducting the experiment | : 10 Marks |
| (c) Performance, result and inference (usage of equipments and trouble shooting) | : 25 Marks |
| (d) Viva voce | : 20 marks |
| (e) Record | : 5 Marks |

General instructions: End-semester practical examination is to be conducted immediately after the second series test covering entire syllabus given below. Evaluation is to be conducted under the equal responsibility of both the internal and external examiners. The number of candidates evaluated per day should not exceed 20. Students shall be allowed for the examination only on submitting the duly certified record. The external examiner shall endorse the record.

Part A : List of Experiments using discrete components [Any Six experiments mandatory]

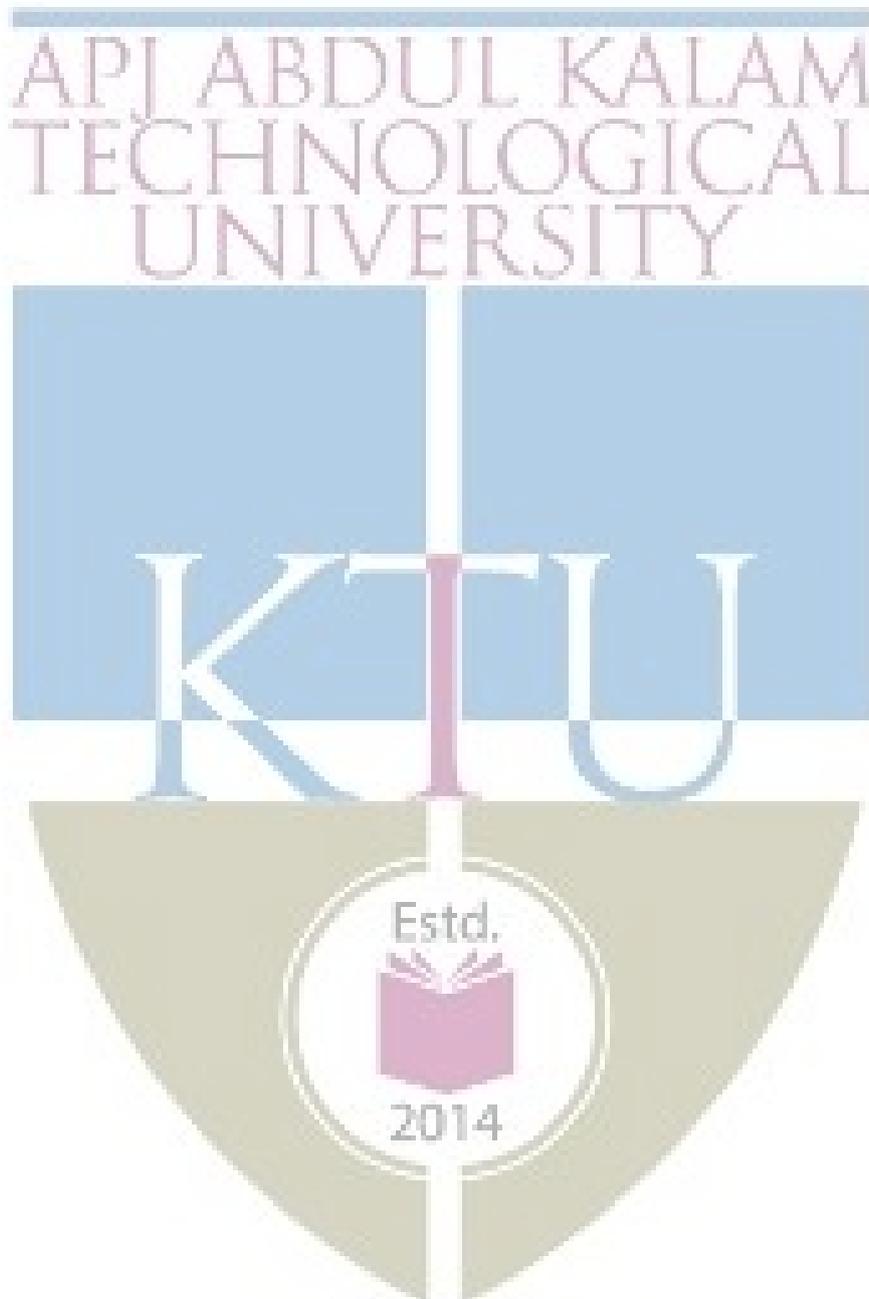
1. RC integrating and differentiating circuits (Transient analysis with different inputs and frequency response)
2. Clipping and clamping circuits (Transients and transfer characteristics)
3. RC coupled CE amplifier - frequency response characteristics
4. MOSFET amplifier (CS) - frequency response characteristics
5. Cascade amplifier – gain and frequency response
6. Cascode amplifier -frequency response
7. Feedback amplifiers (current series, voltage series) - gain and frequency response
8. Low frequency oscillators –RC phase shift or Wien bridge
9. Power amplifiers (transformer less) - Class B and Class AB
10. Transistor series voltage regulator (load and line regulation)

PART B: Simulation experiments [Any Six experiments mandatory]

The experiments shall be conducted using open tools such as QUCS, KiCad or variants of SPICE.

1. RC integrating and differentiating circuits (Transient analysis with different inputs and frequency response)
2. Clipping and clamping circuits (Transients and transfer characteristics)
3. RC coupled CE amplifier - frequency response characteristics
4. MOSFET amplifier (CS) - frequency response characteristics
5. Cascade amplifier – gain and frequency response
6. Cascode amplifier – frequency response

7. Feedback amplifiers (current series, voltage series) - gain and frequency response
8. Low frequency oscillators – RC phase shift or Wien bridge
9. Power amplifiers (transformer less) - Class B and Class AB
10. Transistor series voltage regulator (load and line regulation)



ECL 204	MICROCONTROLLER LAB	CATEGORY	L	T	P	CREDIT
		PCC	0	0	3	2

Preamble: This course aims to

- (i) Familiarize the students with Assembly Language Programming of modern microcontrollers.
- (ii) Impart the skills for interfacing the microcontroller with the help of Embedded C/Assembly Language Programming.

Prerequisite: Nil

Course Outcomes: After the completion of the course the student will be able to

CO 1	Write an Assembly language program/Embedded C program for performing data manipulation.
CO 2	Develop ALP/Embedded C Programs to interface microcontroller with peripherals
CO 3	Perform programming/interfacing experiments with IDE for modern microcontrollers.

Mapping of course outcomes with program outcomes

	PO 1	PO 2	PO 3	PO 4	PO 5	PO 6	PO 7	PO 8	PO 9	PO 10	PO 11	PO 12
CO 1	3		3		3				3			3
CO 2	3		3	2	3				3			3
CO 3	3		3	3	3	3			3		3	3

Assessment

Mark distribution

Total Marks	CIE	ESE	ESE Duration
150	75	75	2.5 hours

Continuous Internal Evaluation Pattern:

Attendance	:	15 marks
Continuous Assessment	:	30 marks
Internal Test (Immediately before the second series test)	:	30 marks

End Semester Examination Pattern: The following guidelines should be followed regarding award of marks

(a) Preliminary work	:	15 Marks
(b) Implementing the work/Conducting the experiment	:	10 Marks
(c) Performance, result and inference (usage of equipments and trouble shooting)	:	25 Marks
(d) Viva voce	:	20 marks

(e) Record

: 5 Marks

General instructions: End-semester practical examination is to be conducted immediately after the second series test covering entire syllabus given below. Evaluation is to be conducted under the equal responsibility of both the internal and external examiners. The number of candidates evaluated per day should not exceed 20. Students shall be allowed for the examination only on submitting the duly certified record. The external examiner shall endorse the record.

PART – A (At least 6 experiments are mandatory)

These experiments shall be performed using 8051 trainer kit. The programs shall be written either in embedded C or in assembly language.

1. Data transfer/exchange between specified memory locations.
2. Largest/smallest from a series.
3. Sorting (Ascending/Descending) of data.
4. Addition / subtraction / multiplication / division of 8/16 bit data.
5. Sum of a series of 8 bit data.
6. Multiplication by shift and add method.
7. Square / cube / square root of 8 bit data.
8. Matrix addition.
9. LCM and HCF of two 8 bit numbers.
10. Code conversion – Hex to Decimal/ASCII to Decimal and vice versa.

PART – B (At least 4 experiments are mandatory.)

Interfacing experiments shall be done using modern microcontrollers such as 8051 or ARM. The interfacing modules may be developed using Embedded C.

1. Time delay generation and relay interface.
2. Display (LED/Seven segments/LCD) and keyboard interface.
3. ADC interface.
4. DAC interface with wave form generation.
5. Stepper motor and DC motor interface.
6. Realization of Boolean expression through port.