



**APJ ABDUL KALAM TECHNOLOGICAL UNIVERSITY**  
Thiruvananthapuram, Kerala, INDIA

**Time Table**

**Exam: B.Tech S3 Supplementary Examination, July 2017**

**(16) Branch: Mechanical Engineering (ME)**

Date	Day	Time	Exam Slot	Course Code	Course Name
04.07.2017	Tue	10.00am-01.00pm	A	MA201	Linear Algebra & Complex Analysis
05.07.2017	Wed	10.00am-01.00pm	B	ME201	Mechanics of Solids
06.07.2017	Thu	10.00am-01.00pm	C	ME203	Mechanics of Fluids
07.07.2017	Fri	10.00am-01.00pm	D	ME205	Thermodynamics
10.07.2017	Mon	10.00am-01.00pm	E	ME210	Metallurgy and Materials Engineering

ME

**(7) Branch: Civil Engineering (CE)**

Date	Day	Time	Exam Slot	Course Code	Course Name
04.07.2017	Tue	10.00am-01.00pm	A	MA201	Linear Algebra & Complex Analysis
05.07.2017	Wed	10.00am-01.00pm	B	CE201	Mechanics of Solids
06.07.2017	Thu	10.00am-01.00pm	C	CE203	Fluid Mechanics I
07.07.2017	Fri	10.00am-01.00pm	D	CE205	Engineering Geology
10.07.2017	Mon	10.00am-01.00pm	E	CE207	Surveying

CE

Date	Day	Time	Exam Slot	Course Code	Course Name
04.07.2017	Tue	10.00am-01.00pm	A	MA201	Linear Algebra & Complex Analysis
05.07.2017	Wed	10.00am-01.00pm	B	CS201	Discrete Computational Structures
06.07.2017	Thu	10.00am-01.00pm	C	CS203	Switching Theory and Logic Design
07.07.2017	Fri	10.00am-01.00pm	D	CS205	Data Structures
10.07.2017	Mon	10.00am-01.00pm	E	CS207	Electronics Devices & Circuits

CS

Date	Day	Time	Exam Slot	Course Code	Course Name
04.07.2017	Tue	10.00am-01.00pm	A	MA201	Linear Algebra & Complex Analysis
05.07.2017	Wed	10.00am-01.00pm	B	EE201	Circuits and Networks
06.07.2017	Thu	10.00am-01.00pm	C	EE203	Analog Electronic Circuits
07.07.2017	Fri	10.00am-01.00pm	D	EE205	DC Machines and Transformers
10.07.2017	Mon	10.00am-01.00pm	E	EE207	Computer Programming

EE

Date	Day	Time	Exam Slot	Course Code	Course Name
04.07.2017	Tue	10.00am-01.00pm	A	MA201	Linear Algebra & Complex Analysis
05.07.2017	Wed	10.00am-01.00pm	B	EC201	Network Theory
06.07.2017	Thu	10.00am-01.00pm	C	EC203	Solid State Devices
07.07.2017	Fri	10.00am-01.00pm	D	EC205	Electronic Circuits
10.07.2017	Mon	10.00am-01.00pm	E	EC207	Logic Circuit Design

EC